1.0 A LOW DROPOUT REGULATOR WITH VOLTAGE CORRECTION CIRCUIT

■ FEATURES

- Output Current 1.0 A
  Maximum Output Current 1.5 A
  FAULT Detection Current 1.4 A
- Default Output Voltage Accuracy 5.1 V ±1%
- Adjustable Output Voltage 5.15 V ±1.5%
- Output Voltage Correction
  V_O + 250 mV (typ), 0 mA to 1000 mA at R_{ADJ} = 10 kΩ
- Internal Fixed Soft-Start 4 msec (typ)
- Error Flag Output (FAULT)
- Error Flag Output Delay for Hot Plug 1.2 msec (typ)
- ON/OFF Control
- Overcurrent Protection
- Short-Circuit Protection
- Reverse Current Prevention
- Undervoltage Lockout
- Thermal Shutdown Circuit with Hysteresis
- Package HSOP8

■ APPLICATIONS

- Car Audio / Navigation Systems
- Docking Stations
- USB Chargers

■ DESCRIPTION

The NJM2815 is a low dropout regulator with an internal voltage correction circuit which delivers up to 1.0 A of output current.

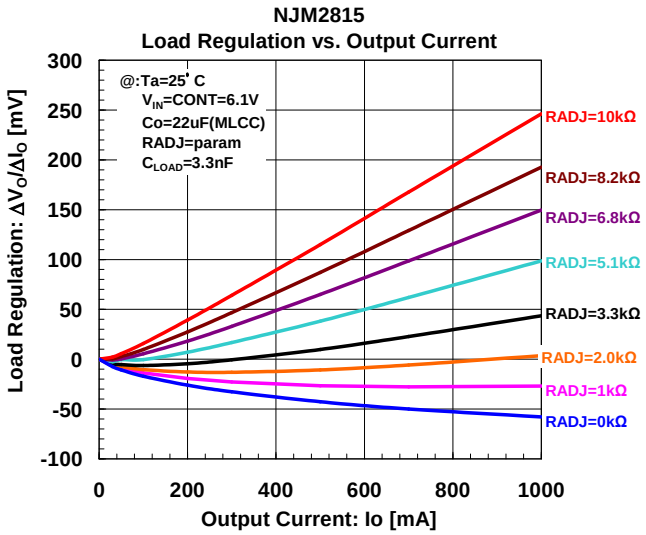
The voltage correction (cable drop compensation) raises the output voltage in proportion to the load current. This function corrects the voltage drop due to cable or writing board resistance.

Unique features, such as overcurrent protection, thermal shutdown, soft-start and error flag output make the NJM2815 ideal for portable devices charged with USB cable.

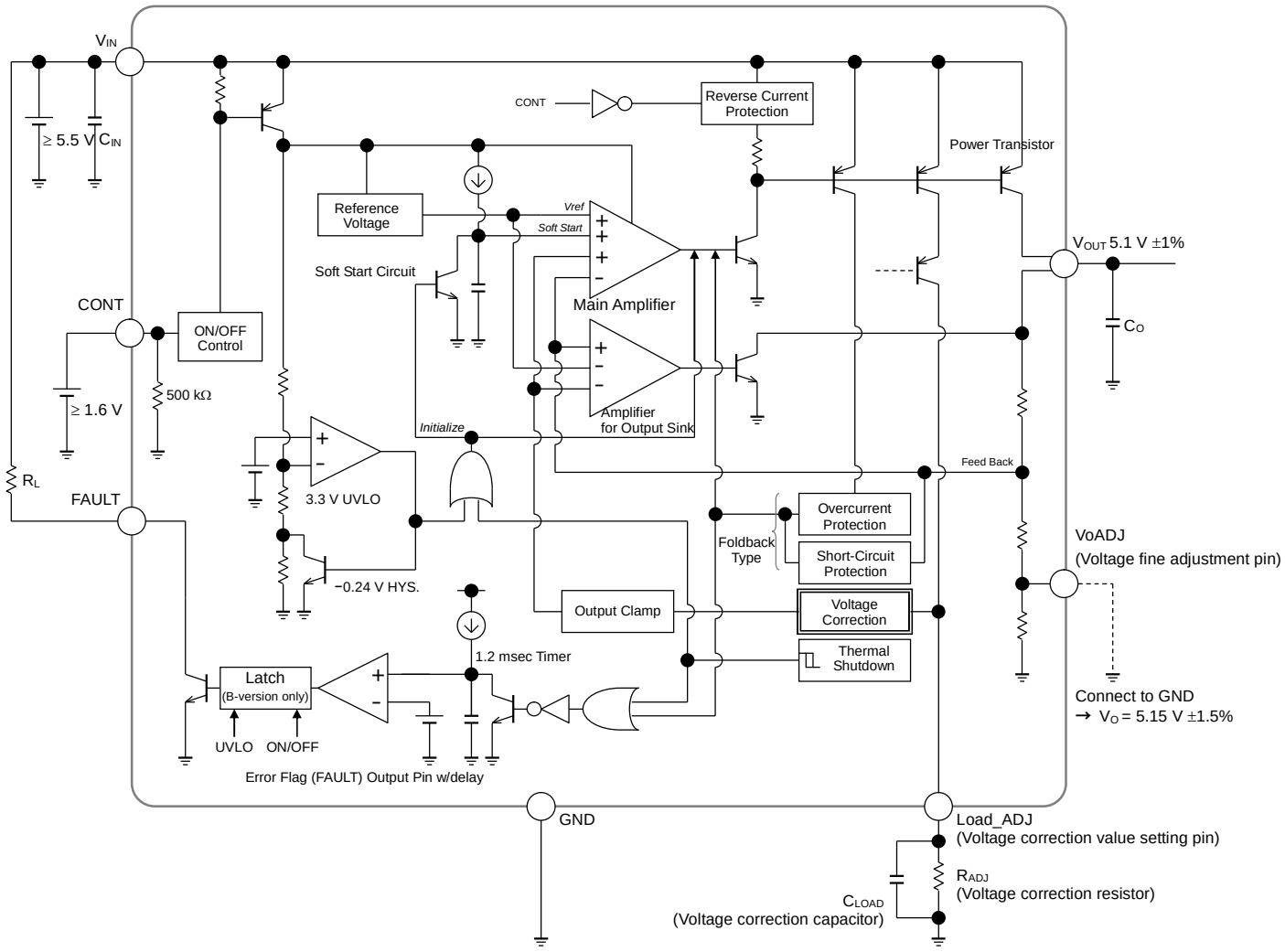
■ PRODUCT VERSION

PRODUCT NAME	FAULT PIN OUTPUT TYPE
NJM2815GM1-51A	Automatic recovery type
NJM2815GM1-51B	Latch type

■ VOLTAE CORRECTION CHARACTERISTICS



■ BLOCK DIAGRAM



The available output current is limited by HSOP8 package power dissipation. Input the supply voltage considering power dissipation at operating temperature. To supply I_O = 1.0 A at T_a = 85°C, V_{IN} = 6 V is recommended.

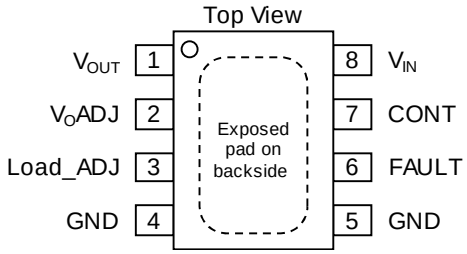
Reverse Current Prevention

The NJW2815 has an internal reverse current prevention circuit that prevents the excessive current to flow the IC when the input pin voltage becomes lower than the output pin voltage. Therefore, no external Schottky barrier diode measure is required.

■ PIN CONFIGURATION

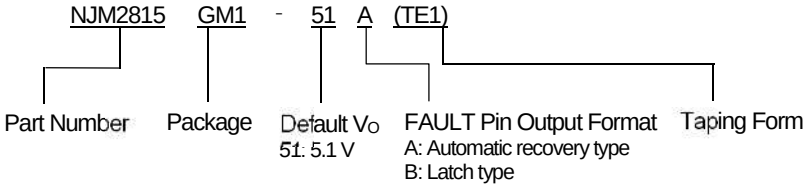
HSOP8

Top View



PIN NO.	SYMBOL	DESCRIPTION
1	V_{OUT}	Output pin
2	V_OADJ	Output adjustment pin
3	Load_ADJ	Voltage correction adjustment pin
4	GND	Ground pin
5	GND	Ground pin
6	FAULT	Error flag output pin
7	CONT	ON/OFF control pin
8	V_{IN}	Input pin

■ PRODUCT NAME INFORMATION



■ ORDERING INFORMATION

PRODUCT NAME	PACKAGE	RoHS	HALOGEN-FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ (pcs)
NJM2815GM1-51A (TE1)	HSOP8	Yes	Yes	Sn 100%	2815NA	81	3000
NJM2815GM1-51B (TE1)	HSOP8	Yes	Yes	Sn 100%	2815NB	81	3000

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATING	UNIT
Input Voltage	V_{IN}	+10	V
Control Voltage	V_{CONT}	+10	V
FAULT Pin Voltage	V_{FAULT}	+10	V
Power Dissipation (T_a = 25°C) HSOP8	P_D	2-Layer (1) / 4-Layer (2) 790 / 2500	mW
Junction Temperature	T_j	−40 to 150	°C
Operating Temperature Range	T_{opr}	−40 to 85	°C
Storage Temperature	T_{stg}	−40 to 150	°C

(1) 2-Layer: Mounted on glass epoxy board (76.2 mm × 114.3 mm × 1.6 mm: based on EIA/JEDEC standard, 2-layer FR-4).
(2) 4-Layer: Mounted on glass epoxy board (76.2 mm × 114.3 mm × 1.6 mm: based on EIA/JEDEC standard, 4-layer FR-4).
(For 4-layer: Applying 74.2 mm × 74.2 mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5.)

■ RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	VALUE	UNIT
Input Voltage	V_{IN}	5.5 to 8	V
Output Current	I_O	0 to 1	A

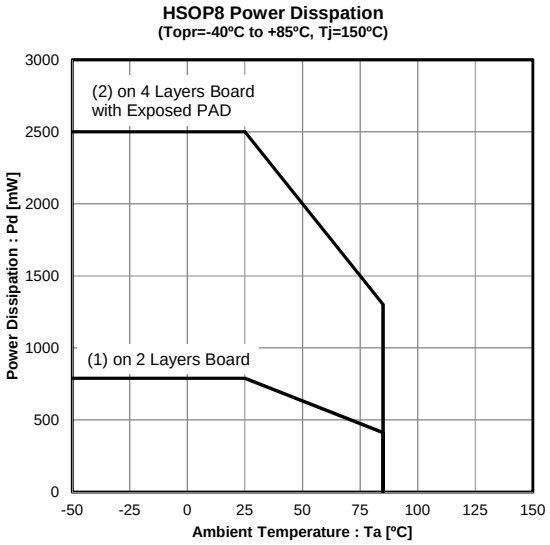
■ **ELECTRICAL CHARACTERISTICS** $V_{IN} = V_O + 1$ V, $C_{IN} = 10$ µF, $C_O = 22$ µF, $T_a = 25°C$, unless otherwise noted.

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
REGULATOR						
Input Voltage	V_{IN}		5.5	-	8	V
Output Voltage	V_O	$I_O = 0$ mA	5.05	5.10	5.15	V
Output Voltage 2	V_O2	$I_O = 0$ mA, V_OADJ = 0 V	5.07	5.15	5.23	V
Average Temperature Coefficient of Output Voltage	$\Delta Vo/\Delta T_a$	$T_a = 0°C$ to $85°C$	-	±50	-	ppm/°C
Load Regulation 1	$\Delta V_O/\Delta I_O1$	$R_{ADJ} = 10$ kΩ, 0 to 1000 mA	+200	+250	+300	mV
Load Regulation 2	$\Delta V_O/\Delta I_O2$	$R_{ADJ} = 6.8$ kΩ, 0 to 1000 mA	+100	+150	+200	mV
Load Regulation 3	$\Delta V_O/\Delta I_O3$	$R_{ADJ} = 5.1$ kΩ, 0 to 1000 mA	+50	+100	+150	mV
Line Regulation	$\Delta V_O/\Delta V_{IN}$	$V_{IN} = 6$ V to 8 V	-	-	10	mV
Dropout Voltage	$\Delta Vi\text{-}_O$	$I_O = 1000$ mA	-	0.20	0.30	V
Quiescent Current	I_Q	$I_O = 0$ mA, except I_{CONT}	-	980	1350	µA
Quiescent Current at Control OFF	$I_{Q\,(OFF)}$	$V_{CONT} = 0$ V	-	-	1	µA
Output Current	I_O	$V_O \times 0.9$	1.5	1.8	-	A
Soft-Start Time	SS	From CONT = high to $V_O \times 0.9$	-	4	8	msec
Control Current	I_{CONT}	$V_{CONT} = 1.8$ V	-	3	12	µA
Output ON Control Voltage	$V_{CONT\,(ON)}$		1.8	-	-	V
Output OFF Control Voltage	$V_{CONT\,(OFF)}$		-	-	0.5	V
Output Clamp Voltage	V_{O_CLAMP}		5.5	-	6.0	V
Reverse Bias Current	I_{O_REV}	$V_{CONT} = 0$ V, $V_O = 5.0$ V, $V_{IN} = 0$ V	-	-	0.5	mA
Power Supply Short-Circuit Voltage	V_ODCIN	Connect series resistance: 1 kΩ	16	-	-	V
UNDERVOLTAGE LOCKOUT (UVLO)						
V_{IN} Undervoltage Lockout Threshold Voltage	V_{IN}UVLO	V_{IN}: SWEEP UP	3.1	3.3	3.5	V
V_{IN} Undervoltage Lockout Hysteresis Voltage	V_{IN}HYS	V_{IN}: SWEEP DOWN	180	240	300	mV
FAULT OUTPUT						
Low-Level Output Voltage	VORL	$R_L = 100$ kΩ	10	100	300	mV
FAULT Output Current at ON-State	IORL	$R_L = 0$ kΩ	5	100	220	mA
FAULT Output Delay Time	Td		-	1.2	2.8	msec
FAULT Detection Current	I_O_det		1.4	1.7	-	A

■ THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	VALUE	UNIT
Junction-to-Ambient Thermal Resistance HSOP8	θ_{ja}	2-Layer (1) / 4-Layer (2) 158 / 50	°C/W
Junction-to-Top of Package Characterization Parameter HSOP8	Ψ_{jt}	2-Layer (1) / 4-Layer (2) 28 / 12	°C/W

■ POWER DISSIPATION vs. AMBIENT TEMPERATURE



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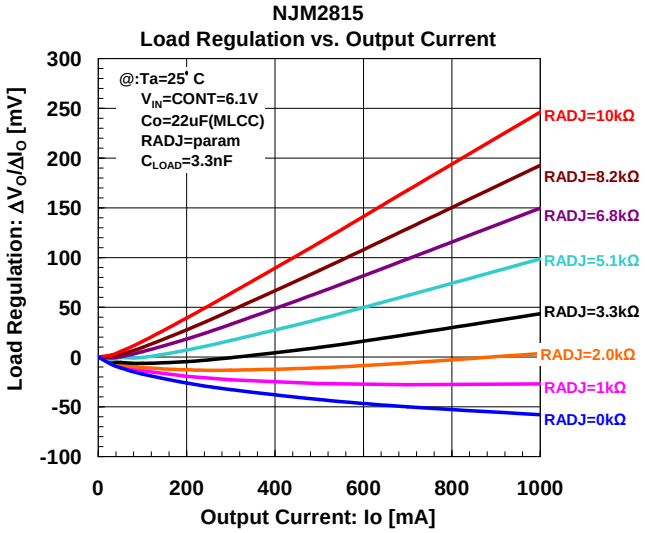
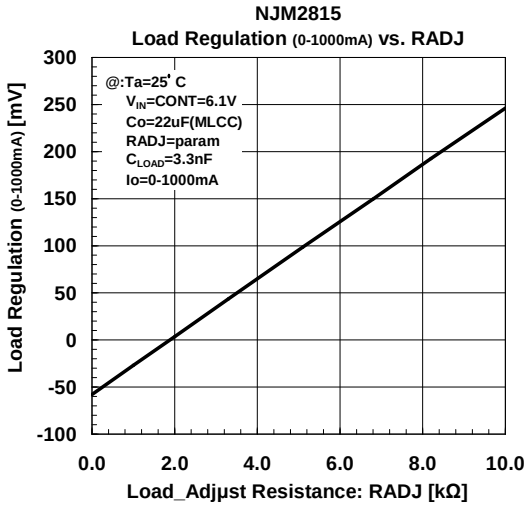
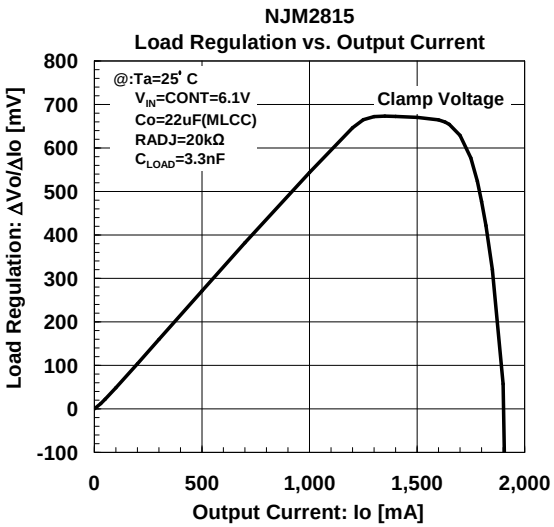
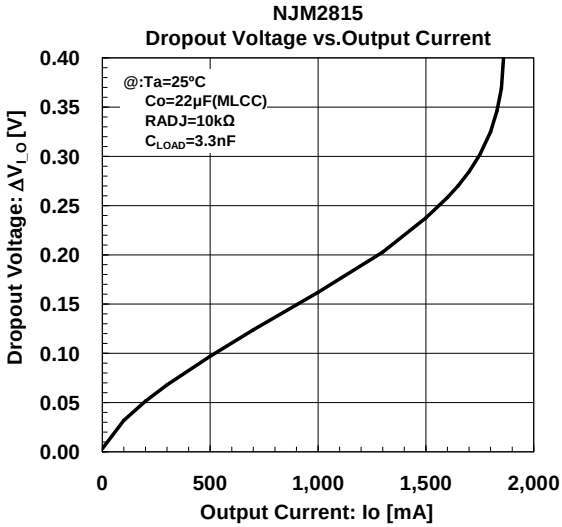
(2) 4-Layer: Mounted on glass epoxy board (76.2 mm × 114.3 mm × 1.6 mm: based on EIA/JEDEC standard, 4-layer FR-4).
(For 4-layer: Applying 74.2 mm × 74.2 mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5.)

■ TYPICAL CHARACTERISTICS

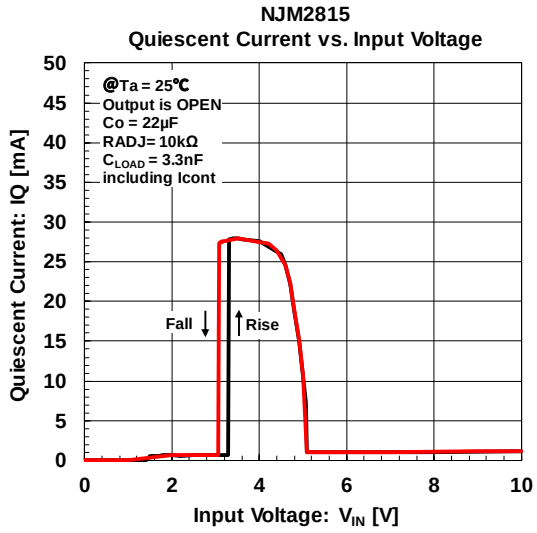


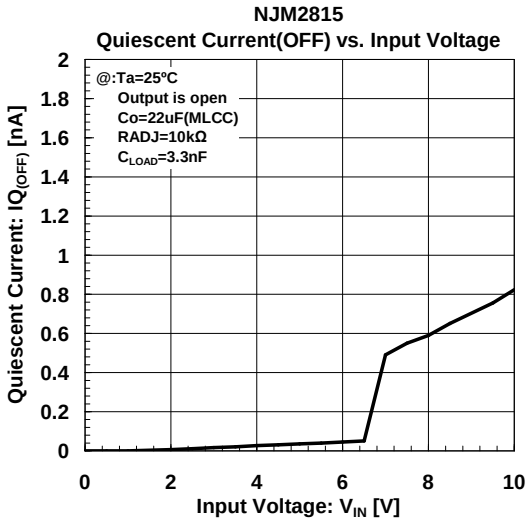


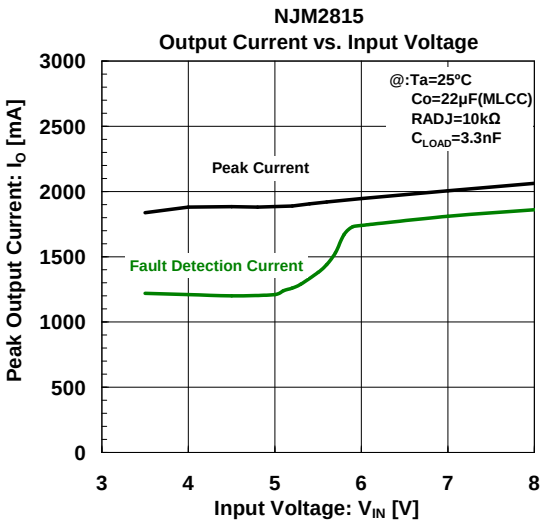


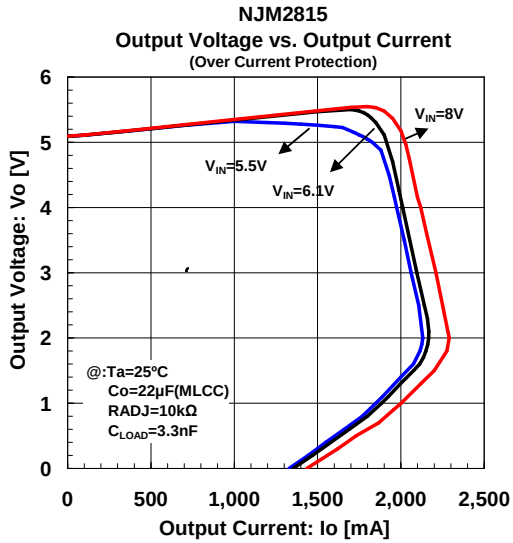


■ TYPICAL CHARACTERISTICS

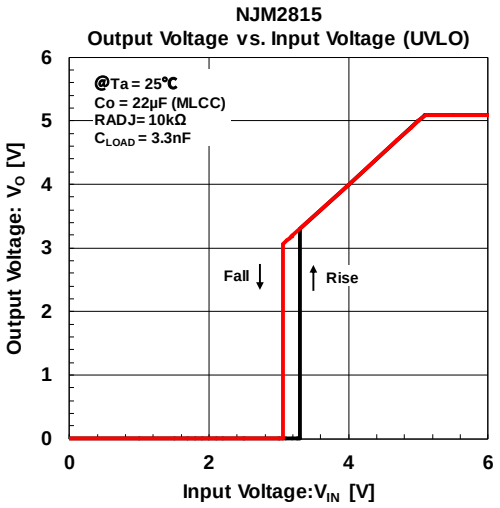


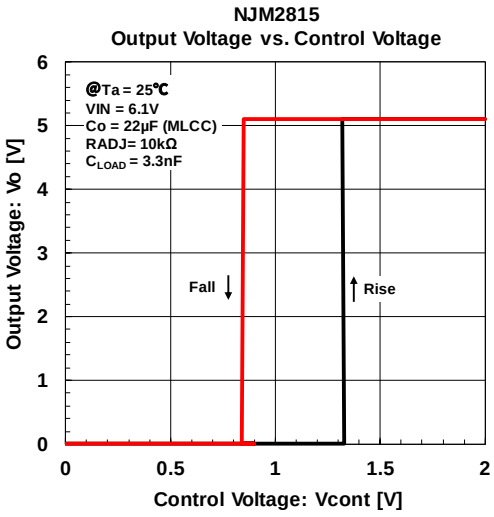


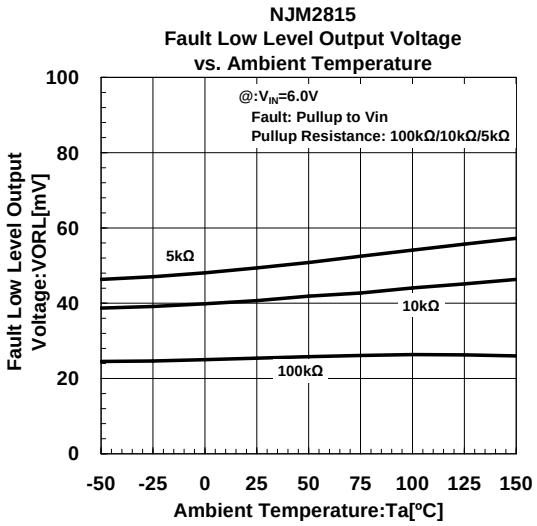


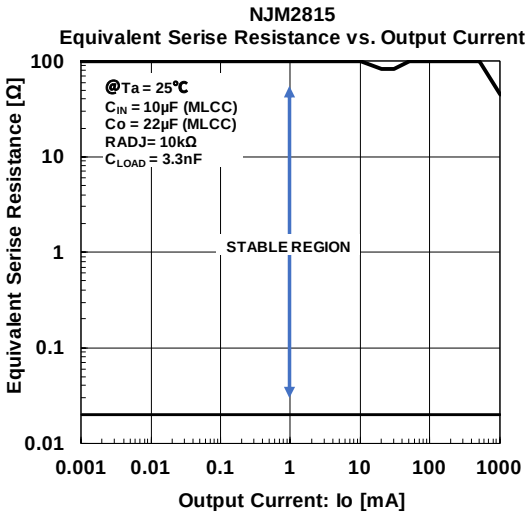


■ TYPICAL CHARACTERISTICS

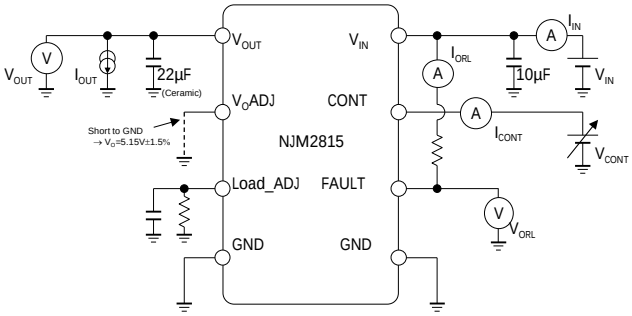




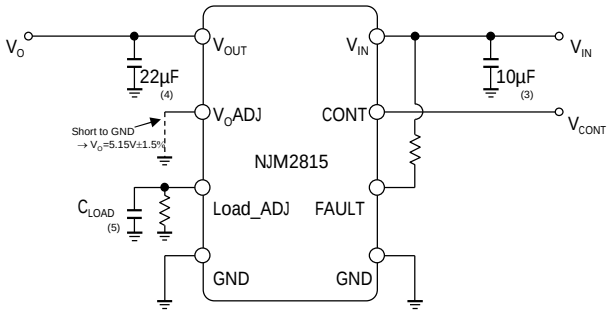




■ MEASUREMENT CIRCUIT



■ TYPICAL APPLICATION



Input Capacitor (C_{IN})

The C_{IN} prevents oscillations and reduce power supply ripple of applications when the power supply impedance is high or power supply line is long. Connecting a 10 µF or larger C_{IN} between V_{IN} and GND pins as short path as possible.

Output Capacitor (C_O)

C_O is necessary for phase compensation of the error amplifier built in the regulator, and the capacitance value and ESR (Equivalent Series Resistance) affect the stability of the circuit. If a C_O with a capacitance value of less than 22 µF is used, output noise and/or regulator oscillation may occur due to lack of the phase compensation. For stable operation, connect a 22 µF or larger C_O between the V_{OUT} and GND pins as short path as possible. Unlike typical products, the NJM2815 has internal voltage correction that controls the output voltage to increase in proportion to the output current. In the process of raising the voltage, the C_O that is larger than necessary can cause a decrease in correction speed and/or oscillations.

Select the output capacitor considering various characteristics such as frequency characteristics, temperature characteristics, and DC bias characteristics. For the C_O, a capacitor with excellent temperature characteristics and sufficient margin for output voltage is recommended.

Load_ADJ Capacitor (C_{LOAD})

The output voltage increases in proportional to the output current (positive feedback control). C_{LOAD} is required to reduce the speed of positive feedback control and suppress oscillations. The smaller C_{LOAD}, the faster the voltage correction speed when the load fluctuations; however, as the C_O and/or the amount of the correction increase, the speed of the positive feedback loop needs to be reduced. Perform sufficient evaluation using the actual application to select the C_{LOAD}.

■ **PIN FUNCTIONS**

PIN NO.	SYMBOL	DESCRIPTION
1	V_{OUT}	Regulator output pin. The correction voltage is added to the output voltage (5.1 V ±1%). The IC has an internal overcurrent protection and a short-circuit protection. The output current is limited at 1.5 A. When shorted to GND, the output current is limited at 1.3 A (typ) (design value). Connect a $C_O \geq 22$ µF, between this pin and the GND pin to prevent output oscillations.
2	V_OADJ	Output voltage adjustment pin. The output voltage becomes 5.1 V ±1% at open, and when connected to GND potential, it becomes 5.15 V ±1.5%. Leave this pin open when not in use.
3	Load_ADJ	Voltage correction (load regulation) adjustment pin. The load regulation (0 mA to 1000 mA) at R_{ADJ} = 10 kΩ is +250 mV (typ). Connect the C_{LOAD} in parallel to R_{ADJ} to prevent oscillations caused by voltage correction. The clamp voltage (6.0 V, max) is not depending on R_{ADJ}.
4, 5	GND	Ground pin.
6	FAULT	Error flag output pin. If an error is detected, a low signal is output. Connect to a power line via pull-up resistor because it is an open corrector pin of the NPN transistor. The error flag is output during overcurrent protection, short-circuit protection, or thermal shutdown. This device has output delay time of 1.2 msec (typ). There are two type output formats: A-version (automatic recovery type) and B-version (latch type).
7	CONT	ON/OFF control pin. It is high active. This pin is pulled down with 500 kΩ (typ) and fixed to OFF state when the CONT pin is open.
8	V_{IN}	Power supply pin. Input Capacitor (C_{IN}) is required to prevent oscillations due to rise of the power supply impedance. Connect a capacitor with a sufficiently low ESR and $C_{IN} \geq 10$ µF between this pin and the GND pin as short path as possible.
-	Exposed pad	Connected to the 4 pin and the 5 pin of GND pin.

■ APPLICATION NOTE

Main Amplifier

This amplifier is an error amplifier that controls the power transistor by comparing the internal reference voltage and the output voltage (V_O). This amplifier has a function to adjust the offset voltage according to the Load_ADJ pin voltage. By this function, a drop voltage due to the influence of cable impedance is corrected. The phase compensation allows to use wide range of output capacitors from 0.02 Ω ≤ ESR ≤ 30 Ω with C_O = 22 µF, C_{LOAD} = 3.3 nF. See the *Output Stability* on page 16.

Output Sink Amplifier

This circuit discharges the electrical charge of output capacitor to prevent output voltage overshoot when load is open.

Soft-Start

The V_O pin voltage is slowly increased when the CONT pin goes from low to high or V_{IN} rises above the UVLO threshold voltage (3.3 V, typ), and the rush current to the output capacitor is reduced. Soft-start is also enabled when the thermal shutdown is released. The soft-start time is defined as the time from CONT = high to rising to V_O × 0.9, which is 4msec (typ).

ON/OFF Control

This circuit controls regulator ON/OFF. The quiescent current at OFF is 1 µA (max). This pin can be controlled by CMOS logic voltage, thus the CONT pin is high impedance. The CONT pin is pulled down with internal resistor of 500 kΩ (typ). At the OFF state, the error flag (FAULT pin = low) is not output.

V_{IN} Undervoltage Lockout (UVLO)

This circuit prevents malfunction by locking out the output when V_{IN} is below the UVLO detection voltage. The output voltage rises when the input voltage rises and exceeds the V_{IN} undervoltage lockout threshold voltage (3.3 V, typ). When the input voltage drops below the UVLO detection voltage (3.06 V, typ)*, the output voltage falls. The error flag (FAULT pin = low) is not output during lockout.

*UVLO detection voltage (3.06 V, typ) = V_{IN} undervoltage lockout threshold voltage (3.3 V, typ) − V_{IN} undervoltage lockout hysteresis voltage (0.24 V, typ)

Overcurrent Protection / Short-Circuit Protection

The NJM2815 has an overcurrent protection and a short-circuit protection. The overcurrent protection characteristic is foldback current limit. This circuit outputs an error flag (FAULT pin = low) in either case of the over current protection or the short-circuit protection operates.

Thermal Shutdown

When the NJM2815 chip (junction) temperature exceeds 170°C, the output is turned off and when the chip temperature decrease less than 145°C the output is turned on. If the overload condition continues constantly, the output repeats ON/OFF, so the output voltage may appear to oscillate greatly. This function is built-in to protect the IC; therefore, do thermal design below T_{jmax}. When the thermal shutdown operates, the error flag (FAULT pin = low) is output.

Voltage Correction

This circuit generates a correction voltage proportional to the output current.

Error Flag (FAULT) Output Circuit with Delay Function

This circuit operates during overcurrent protection, short-circuit protection, or thermal shutdown. Flowing a current to the external pull-up resistor R_L connected to the FAULT pin and outputs an error flag (FAULT pin = low). An error flag is output 1.2 msec (typ) after entering the protection state and the rush current when a USB device is connected can be ignored. The current capability of the FAULT pin is 5 mA (min). There are two versions of the error flag.

- A-version (automatic recovery type)
  An error flag is output 1.2 msec (typ) after entering the protection state. When the protection is released, the error flag is canceled immediately.
- B-version (latch type)
  An error flag is output 1.2 msec (typ) after entering the protection state, and latched at the same time. When V_{IN} or CONT is turned on again, the error flag is released.

Reverse Current Prevention

The NJW2815 has an internal reverse current prevention circuit that prevents the excessive current to flow the IC when the input pin voltage becomes lower than the output pin voltage. Therefore, no external Schottky barrier diode measure is required. The error flag is not output if reverse current flows.

V_OADJ (Output Voltage Adjustment) Pin

With connecting this pin to GND, the output voltage can be adjusted from 5.1 V ±1% to 5.15 V ±1.5%.

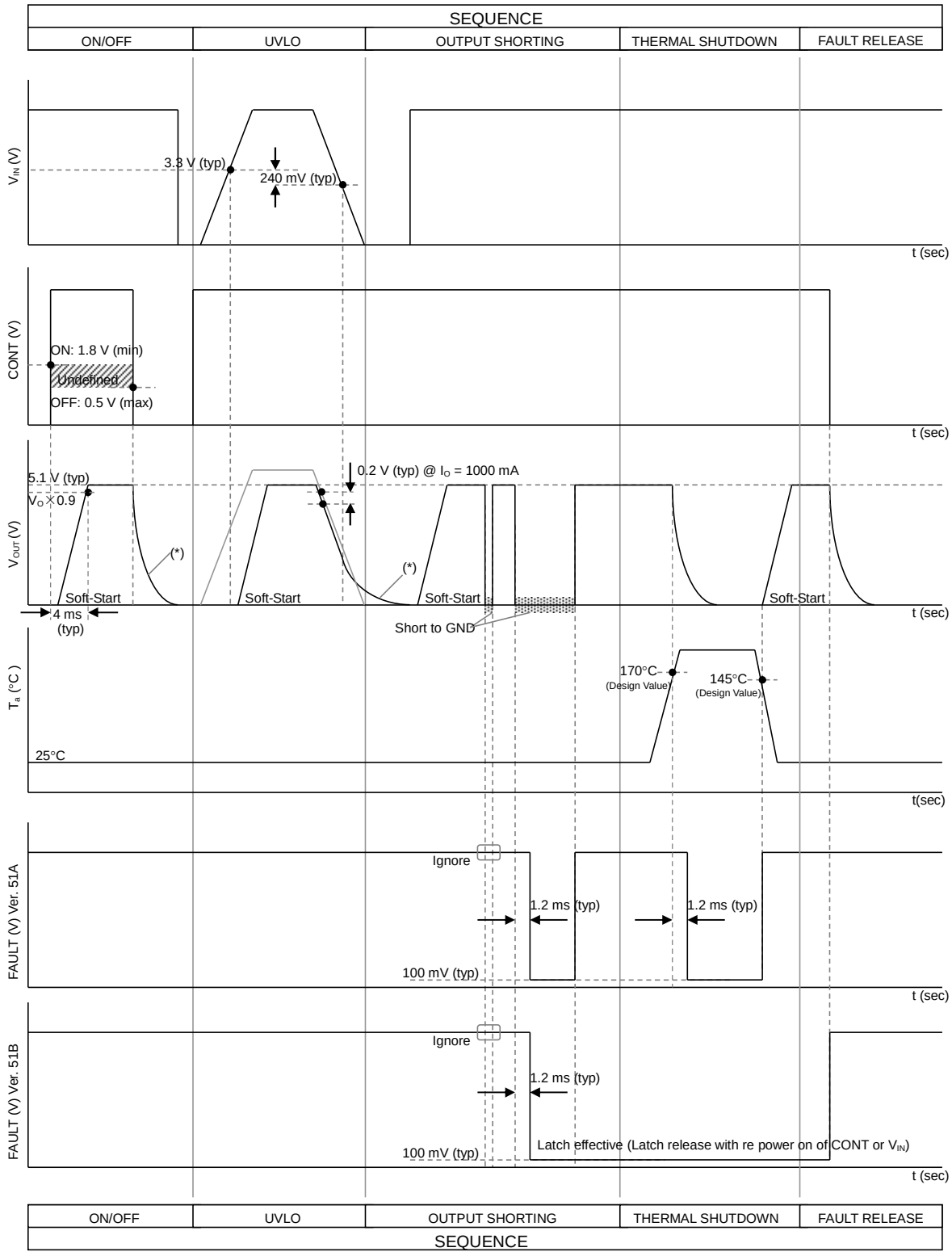
Load_ADJ Pin

A current proportional to the amount of power transistor current is output to this pin. The slope of load regulation can be adjusted by changing the resistor value (R_{ADJ}) connected to this pin. The load regulation (0 mA to 1000 mA) at R_{ADJ} = 10 kΩ is +250 mV (typ). See the Table 1 on page 15 for more information on selecting R_{ADJ}. Connect the C_{LOAD} in parallel to R_{ADJ} to prevent oscillations caused by voltage correction.

Output Clamp

This circuit prevents a load breakdown due to excessive rising of output voltage when the correction is too strong or the R_{ADJ} resistance is removed. The output voltage is clamped at 5.5 V to 6.0 V.

Timing Chart



*The NJM2815 does not have an output capacitor discharge function in the OFF state and UVLO detection.

Voltage Correction (Load Regulation) Adjustment

The NJM2815 can adjust the load regulation by R_{ADJ} connected to Load_ADJ pin. The R_{ADJ} can be calculated using the following formula or refer to Table 1.

Table1. R_{ADJ} (kΩ) vs. Load Regulation Correspondence Table (0 mA to 1000 mA) @25°C

R_{ADJ} (kΩ)	Load Regulation (mV) 0 mA to 1000 mA
1.9	1
2.0	4
2.1	7
2.2	10
2.3	13
2.4	16
2.5	19
2.6	22
2.7	25
2.8	28
2.9	31
3.0	34
3.1	37
3.2	41
3.3	44
3.4	47
3.5	50
3.6	53
3.7	56
3.8	59
3.9	62
4.0	65
4.1	68
4.2	71
4.3	74
4.4	78
4.5	81
4.6	84
4.7	87
4.8	90
4.9	93
5.0	96
5.1	99
5.2	102
5.3	105
5.4	108
5.5	111
5.6	114
5.7	118
5.8	121
5.9	124

R_{ADJ} (kΩ)	Load Regulation (mV) 0 mA to 1000 mA
6.0	127
6.1	130
6.2	133
6.3	136
6.4	139
6.5	142
6.6	145
6.7	148
6.8	151
6.9	155
7.0	158
7.1	161
7.2	164
7.3	167
7.4	170
7.5	173
7.6	176
7.7	179
7.8	182
7.9	185
8.0	188
8.1	191
8.2	195
8.3	198
8.4	201
8.5	204
8.6	207
8.7	210
8.8	213
8.9	216
9.0	219
9.1	222
9.2	225
9.3	228
9.4	232
9.5	235
9.6	238
9.7	241
9.8	244
9.9	247
10.0	250

R_{ADJ} (kΩ)	Load Regulation (mV) 0 mA to 1000 mA
10.1	253
10.2	256
10.3	259
10.4	262
10.5	265
10.6	268
10.7	272
10.8	275
10.9	278
11.0	281
11.1	284
11.2	287
11.3	290
11.4	293
11.5	296
11.6	299
11.7	302
11.8	305
11.9	309
12.0	312

E-24 Series Resistor Value (extract)

2.0	4
2.2	10
2.4	16
2.7	25
3.0	34
3.3	44
3.6	53
3.9	62
4.3	74
4.7	87
5.1	99
5.6	114
6.2	133
6.8	151
7.5	173
8.2	195
9.1	222
10.0	250
11.0	281
12.0	312

Load Regulation (0 mA to 100 mA) Formula 25°C (typ)

$$\Delta Vo / \Delta Io_{(0-100mA)}(mV) = 30.8 \times RADJ(k\Omega) \times 0.1 - 17$$

Load Regulation (0 mA to 200 mA) Formula 25°C (typ)

$$\Delta Vo / \Delta Io_{(0-200mA)}(mV) = 30.8 \times RADJ(k\Omega) \times 0.2 - 25$$

Load Regulation (0 mA to 300 mA) Formula 25°C (typ)

$$\Delta Vo / \Delta Io_{(0-300mA)}(mV) = 30.8 \times RADJ(k\Omega) \times 0.3 - 32$$

Load Regulation (0 mA to 500 mA) Formula 25°C (typ)

$$\Delta Vo / \Delta Io_{(0-500mA)}(mV) = 30.8 \times RADJ(k\Omega) \times 0.5 - 42$$

Load Regulation (0 mA to 1000 mA) Formula 25°C (typ)

$$\Delta Vo / \Delta Io_{(0-1000mA)}(mV) = 30.8 \times RADJ(k\Omega) - 58$$

Output Stability

The NJM2815 has two feedback loops as shown in Figure 1.

(1) Negative feedback generates a constant voltage that compares the reference voltage and the output voltage of the error amplifier.

(2) Positive feedback generates a corrected output voltage proportional to the output current.

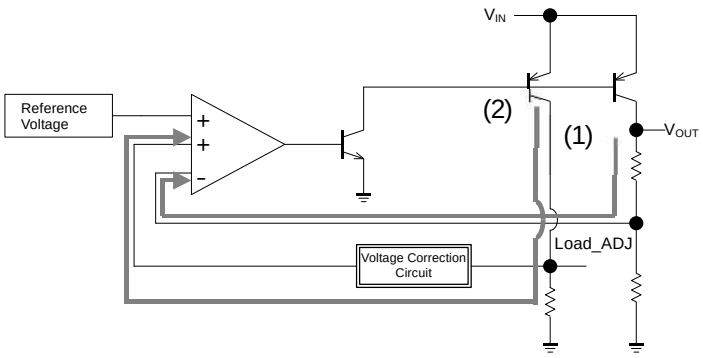


Figure 1. NJM2815 Feedback Loop

When the gain operation of loop (2) is faster and higher than loop (1), the NJM2815 oscillates because the positive feedback control becomes predominant.

The NJM2815 has been confirmed to be stable under the following conditions:

- Output current: 0 mA to 1000 mA
- Output capacitor value: C_O = 22 µF
- ESR of output capacitor: ESR = 0.02 Ω to 30 Ω
- R_{ADJ}: 0 kΩ to 10 kΩ
- C_{LOAD}: 3.3 nF

Use a large C_{LOAD} to reduce the positive feedback loop speed to prevent oscillations under the following conditions.

- When output capacitor (C_O) is larger than 22 µF (Negative feedback loop speed of (1) becomes slow).
- When R_{ADJ} is larger than 10 kΩ (Positive feedback loop gain of (2) becomes high).

The ESR and frequency characteristics depend on C_O type and capacitance. Please evaluate the actual application before use.

Output Pin 16V Battery-to-Short

As shown in Figure 2, under the most severe battery-to-short test condition (V_O = 16 V, V_{IN} = 0 V), a large current may cause the IC to overheat and be damaged. Figure 2 shows the current path. The main path of reverse current is a power transistor.

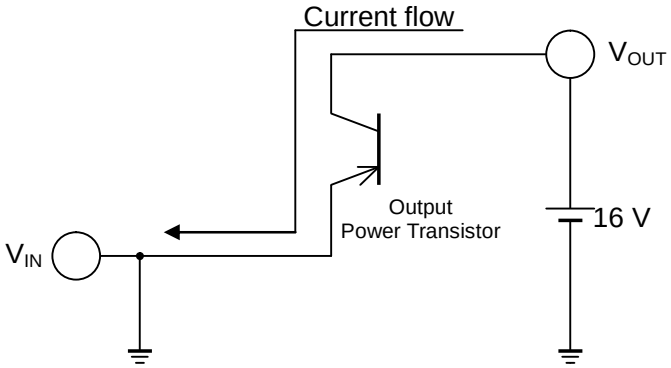


Figure 2. The Most Severe Battery-Short Condition

In a standard application (Figure 3) where the NJM2815 is used as a secondary power supply and a DC/DC converter is installed as a primary power supply, current hardly flows and there is no risk of damage of the IC. Typical power supply ICs are designed with large source capability and small sink capability, and the feedback resistor (a few kΩ to hundreds of kΩ) is the main sink path.

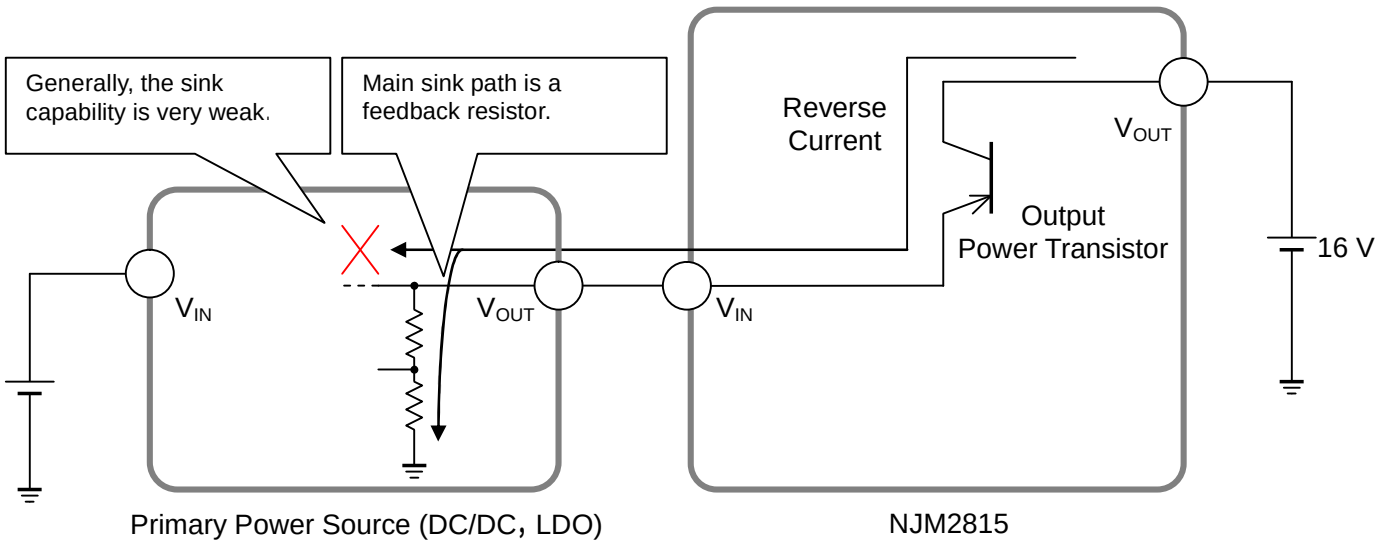


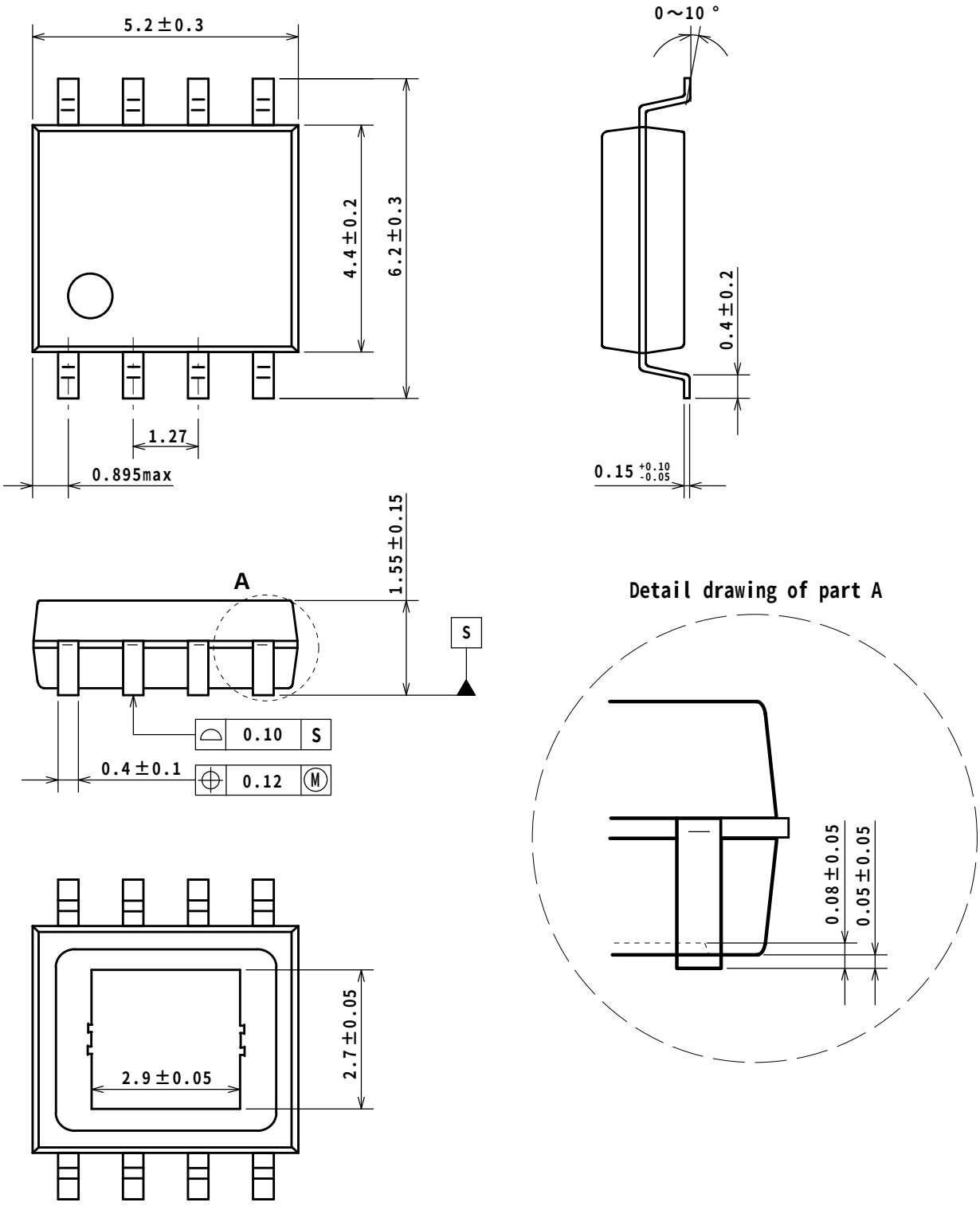
Figure 3. The Battery-Short Current Path of Typical Application

If the NJM2815 V_{IN} pin is shorted to GND as shown in Figure 2, a large current flows and it may cause damage to the IC. In standard application as shown in Figure 3, only a small current flows through the feedback resistor of the primary power supply IC. When the primary power supply IC has no sink capability and the feedback resistor is above 1 kΩ, the IC is not breakdown even if the battery-to -short; however, this is not a recommendation for active use of battery-short protection.

HSOP8

Unit: mm

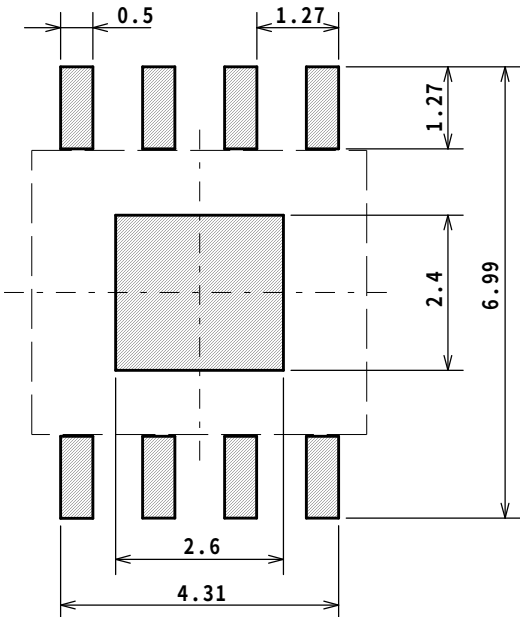
■ PACKAGE DIMENSIONS



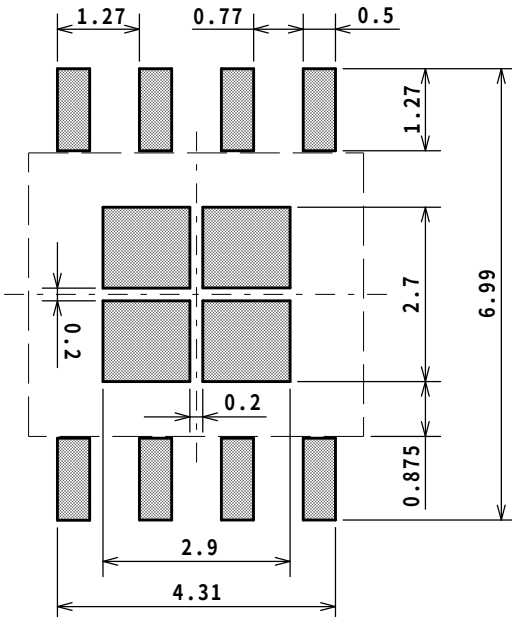
HSOP8

Unit: mm

■ EXAMPLE OF SOLDER PADS DIMENSIONS



<Solder pattern>



<Metal mask>

<Instructions for mounting>

Please note the following points when you mount HSOP-8 package IC because there is a standoff on the backside electrode.

(1) Temperature profile of lead and backside electrode.

It is necessary that both re-flow temperature profile of lead and backside electrodes are higher than preset temperature.

When solder wet temperature is lower than lead/backside electrode temperature, there is possibility of defect mounting.

(2) Design of foot pattern / metal mask

Metal mask thickness of solder pattern print is more than 0.13 mm.

(3) Solder paste

The mounting was evaluated with following solder paste, foot pattern and metal mask.

Because mounting might be greatly different according to the manufacturer and the product number even if the solder composition is the same.

We will strongly recommend to evaluate mounting previously with using foot pattern, metal mask and solder paste.

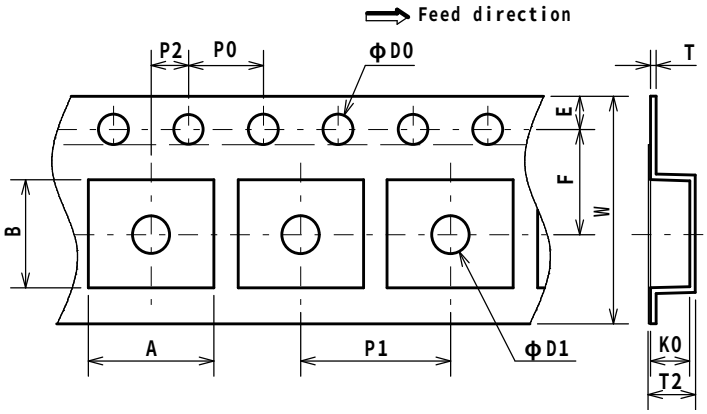
Solder paste composition	Sn37Pb (Senju Metal Industry Co., Ltd：OZ7053-340F-C)
	Sn3Ag0.5Cu (Senju Metal Industry Co., Ltd：M705-GRN350-32-11)

HSOP8

■ PACKING SPEC

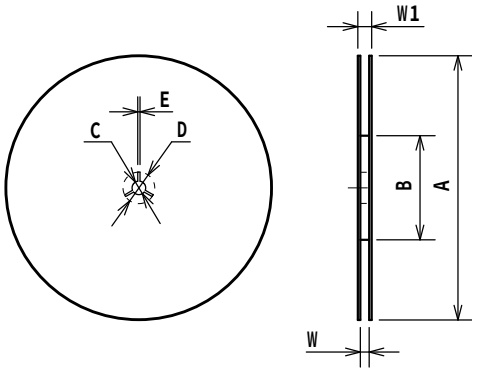
Unit: mm

TAPING DIMENSIONS



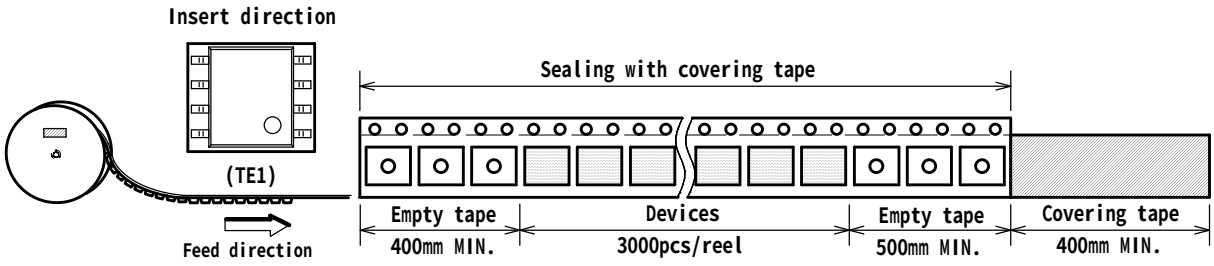
SYMBOL	DIMENSION	REMARKS
A	6.7±0.1	
B	5.55±0.1	
D0	1.55±0.05	
D1	2.05±0.05	
E	1.75±0.1	
F	5.5±0.05	
P0	4.0±0.1	
P1	8.0±0.1	
P2	2.0±0.05	
T	0.3±0.05	
T2	2.47	
K0	2.1±0.1	
W	12.0±0.2	

REEL DIMENSIONS

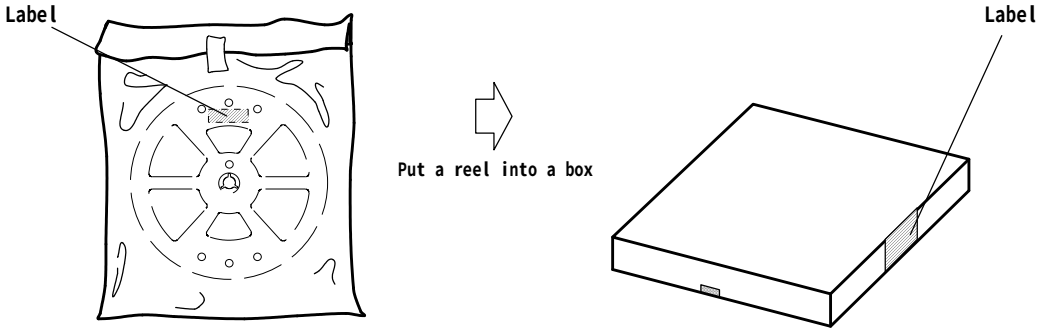


SYMBOL	DIMENSION
A	φ330±2
B	φ 80±1
C	φ 13±0.2
D	φ 21±0.8
E	2±0.5
W	13.5±0.5
W1	17.5±1

TAPING STATE

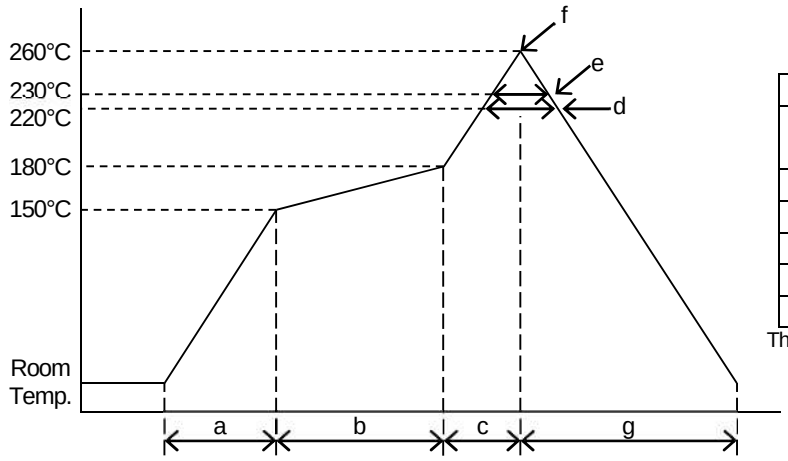


PACKING STATE



■ RECOMMENDED MOUNTING METHOD

INFRARED REFLOW SOLDERING PROFILE



a	Temperature ramping rate	1 to 4°C/s
b	Pre-heating temperature Pre-heating time	150 to 180°C 60 to 120s
c	Temperature ramp rate	1 to 4°C/s
d	220°C or higher time	shorter than 60s
e	230°C or higher time	shorter than 40s
f	Peak temperature	lower than 260°C
g	Temperature ramping rate	1 to 6°C/s

The temperature indicates at the surface of mold package.

■ REVISION HISTORY

DATE	REVISION	CHANGES
November 25, 2019	Ver.1.0	Initial release due to datasheet format change

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