

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

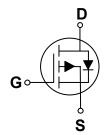
BV _{DSS}	R _{DS(ON)}	Ι _D
-40 V	15 mΩ	-10 A

Features

- $R_{DS(ON)} \le 15\Omega m@V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

SOP-8 Pin Configuration





Applications

- · MB / VGA / Vcore
- · LED Application
- · Load Switch
- POL Applications

osolute Maximi	um Ratings T _c =25°C unless otherwise noted				
Symbol	Parameter Rating				
V_{DS}	Drain-Source Voltage	-40	V		
V_{GS}	Gate-Source Voltage	±20	V		
ı	Drain Current - Continuous (T _C =25°C)	-10	Α		
I _D	Drain Current - Continuous (T _C =100°C)	-6.3	A		
I _{DM}	Drain Current - Pulsed (NOTE 1)	-40	Α		
P_{D}	Power Dissipation (T _C =25°C)	4.2	W		
ı D	Power Dissipation - Derate above 25°C	0.034	W/°C		
T _J	Operating Junction Temperature Range	-55 to 150	°C		
T _{STG}	Storage Temperature Range	-55 to 150	°C		
Marking Code		PD015 , DS4903			

Thermal Characteristics					
Symbol	Parameter	Тур.	Max.	Unit	
$R_{\theta JA}$	Thermal Resistance Junction to Ambient		60	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction to Case		30	°C/W	





Electrical Characteristics (T_J=25°C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0V , I_D = -250uA	-40			V
I _{DSS}	IDrain-Source Leakage Current	V_{DS} = -40V , V_{GS} = 0V , T_{J} =25°C			-1	uA
		V_{DS} = -32V , V_{GS} = 0V , T_{J} =125 $^{\circ}$ C			-10	uA
I_{GSS}	Gate-Source Leakage Current	V_{GS} = ±20V , V_{DS} = 0V			±100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
R _{DS(ON)}	IStatic Drain-Source On-Resistance	V_{GS} = -10V , I_D = -10A		11.5	15	mΩ
		V_{GS} = -4.5V , I_D = -8A		16	22	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = -250 uA$	-1.0	-1.6	-2.5	V
gfs	Forward Transconductance	$V_{DS} = -10V$, $I_{D} = -10A$		13		S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Q_g	Total Gate Charge	V _{DS} = -32V , V _{GS} = -4.5V ,		22.2	40	
Q_{gs}	Gate-Source Charge	I _D = -10A		8.2	16	nC
Q_{gd}	Gate-Drain Charge	(NOTE 2 · 3)		8.8	16	
$T_{d(on)}$	Turn-On Delay Time	V 00V V 40V		23	40	
T_r	Rise Time	V_{DD} = -20V , V_{GS} = -10V , R_{G} = 6Ω , I_{D} = -1A		10	20	nS
$T_{d(off)}$	Turn-Off Delay Time	(NOTE 2 \ 3)		135	250	113
T _f	Fall Time	(1012 0)		46	90	
C _{iss}	Input Capacitance			2757	4000	
C _{oss}	Output Capacitance	V_{DS} = -25V , V_{GS} = 0V , F= 1MHz		240	360	pF
C _{rss}	Reverse Transfer Capacitance]		137	200	

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	-V _G = V _D = 0V,Force Current -	-		-10	Α
I _{SM}	Pulsed Source Current				-20	Α
V_{SD}	Diode Forward Voltage	V_{GS} = 0V , I_{S} = -1A , T_{J} = 25 $^{\circ}$ C			-1	V

NOTES:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width $\leq 300 \text{us}$, duty cycle $\leq 2\%$.
- ${\it 3. Essentially independent of operating temperature.}\\$





Characteristics Curves

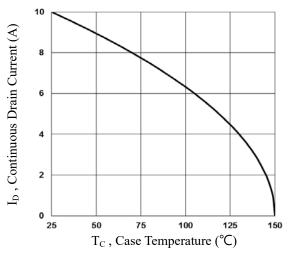


Fig.1 Continuous Drain Current vs. T_c

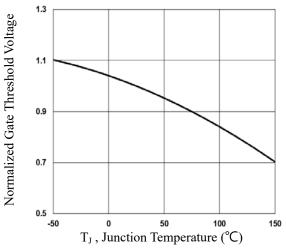


Fig.3 Normalized V_{th} vs. T_J

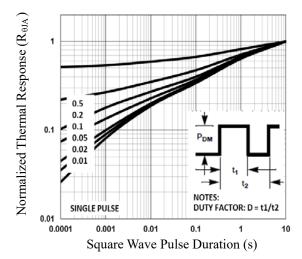


Fig.5 Normalized Transient Impedance

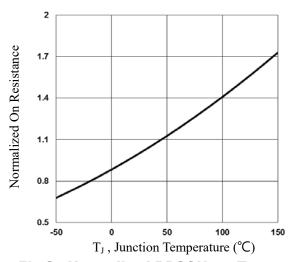


Fig.2 Normalized RDSON vs. T_J

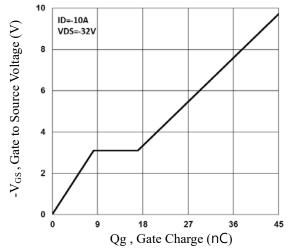


Fig.4 Gate Charge Waveform

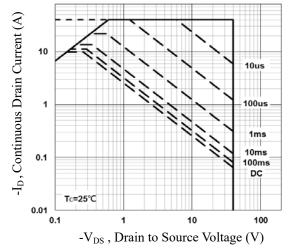
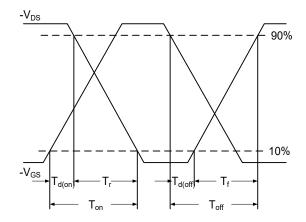


Fig.6 Maximum Safe Operation Area





Characteristics Curves



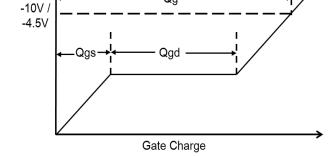
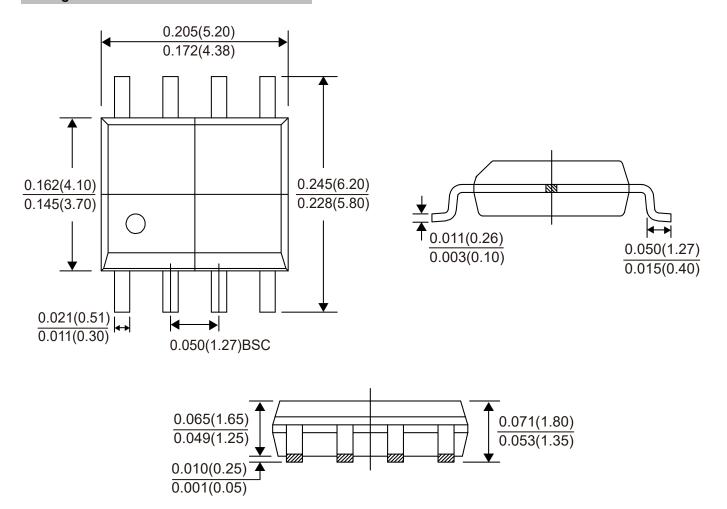


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

-V_{GS}

Package Outline Dimensions



SOP-8Dimensions in inches and (millimeters)





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