

R1171x SERIES

2.0A/1.5A LDO REGULATOR

NO. EA-125-220908

OUTLINE

The R1171x Series are CMOS-based positive voltage regulator ICs. The R1171x Series have features of low dropout voltage, high output voltage accuracy, low consumption current. Each of these ICs consists of a voltage reference unit, an error amplifier, resistor net for setting output voltage, a current limit circuit at short mode, a chip enable circuit, and thermal shutdown circuit. The output voltage of R1171 is fixed in the IC.

Low consumption current by the merit of CMOS process and built-in transistors with low ON-resistance make low dropout voltage and chip enable function prolongs the battery life. These regulators are remarkable improvement on the current regulators in terms of input transient response, and load transient response.

Thus, the R1171x Series are suitable for various power sources.

Since the packages for these ICs are high wattage HSOP-6J package, TO-252-5-P1, high density mounting of the ICs on boards is possible.

FEATURES

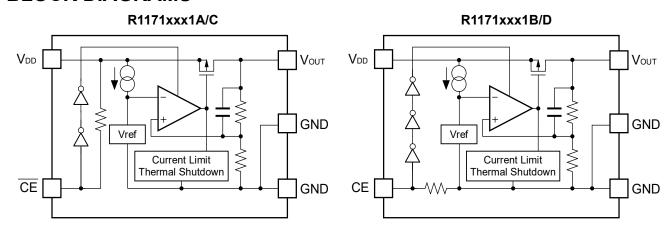
Supply Current	Typ. 130μA
Standby Current	Typ. 0.1μA
Output Current	Min. 1.5A (Vın=Vоuт+1.0V, R1171Sxx1A/B)
	Min. 2.0A (V _{IN} =V _{OUT} +1.0V, R1171Jxx1C/D)
Input Voltage	2.1V to 6.0V
Output Voltage	1.5V to 5.0V (0.1V steps) (R1171Sxx1A/B)
	1.8V to 5.0V (0.1V steps) (R1171Jxx1C/D)
	(For other voltages, please refer to MARK INFORMATIONS.)
Output Voltage Accuracy	± 2.0%
Dropout Voltage	Typ. 0.09V (Vouт=3.0V, Iouт=300mA)
Temperature-drift Coefficient of Output Voltage	Typ. ± 100ppm/°C
Line Regulation	Typ. 0.05%/V
Packages	HSOP-6J, TO-252-5-P1
Built-in Current Limit Circuit	
Built-in Thermal Shutdown Circuit	
Ceramic capacitor for phase compensation	C _{IN} =C _{OUT} =Ceramic 10μF (V _{OUT} <1.8V)
	C _{IN} =C _{OUT} =Ceramic 4.7μF (V _{OUT} ≥ 1.8V)

APPLICATIONS

- Local Power source for Notebook PC.
- Local Power source for portable appliances, cameras, and videos.
- Local Power source for equipment of battery-use.
- Local Power source for home appliances.

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BLOCK DIAGRAMS



SELECTION GUIDE

The output voltage, chip enable polarity, package for the ICs can be selected at the user's request.

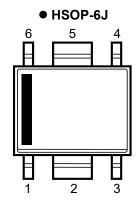
Product Name Package		Quantity per Reel	Pb Free	Halogen Free	
R1171Sxx1*-E2-FE	HSOP-6J	1,000 pcs	Yes	Yes	
R1171Jyy1\$-T1-F	TO-252-5-P1	3,000 pcs	Yes	No	

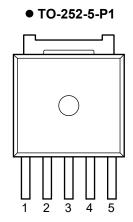
xx: The output voltage can be designated in the range from 1.5V(15) to 5.0V(50) in 0.1V steps. (For other voltages, please refer to MARK INFORMATIONS.)

- yy: The output voltage can be designated in the range from 1.8V(18) to 5.0V(50) in 0.1V steps. (For other voltages, please refer to MARK INFORMATIONS.)
- * : CE pin polarity are options as follows.
 - (A) "L" active
 - (B) "H" active
- \$: CE pin polarity are options as follows.
 - (C) "L" active
 - (D) "H" active

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PIN CONFIGURATIONS





PIN DESCRIPTIONS

• HSOP-6J

Pin No	Symbol	Pin Description
1	Vоит	Output Pin
2	GND	Ground Pin
3	CE or CE	Chip Enable Pin
4	NC	No Connection
5	GND	Ground Pin
6	V _{DD}	Input Pin

• TO-252-5-P1

Pin No	Symbol	Pin Description
1	Vоит	Output Pin
2	CE or CE	Chip Enable Pin
3	GND	Ground Pin
4	GND	Ground Pin
5	V _{DD}	Input Pin

^{*)} No.3 and No.4 pins must be wired short each other and connected to the GND plane when it is mounted on board.

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ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
Vin	Input Voltage	7.0	V
Vce	Input Voltage (CE or CE Input Pin)	-0.3 to V _{IN} +0.3	V
Vоит	Output Voltage	Output Voltage -0.3 to V _{IN} +0.3	
D-	Power Dissipation (HSOP-6J)*1	1700	m///
P _D	Power Dissipation (TO-252-5-P1)*1	1900	mW
Topt	Operating Temperature	-40 to 85	°C
Tstg	Storage Temperature	-55 to 125	°C

^{*)} For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.

The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

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ELECTRICAL CHARACTERISTICS

• R1171Sxx1A Topt=25°C

Symbol	Item	Co	nditions	Min.	Тур.	Max.	Unit
Vout	Output Voltage	VIN-VOUT=1.0V, IOUT=200mA		×0.98		×1.02	V
ΔV ουτ/ ΔΙ ουτ	Load Regulation	VIN-VOUT=1 1mA ≦ IOUT			10	60	mV
			1.5 ≦ Vouт<1.6		0.16	0.35	
			1.6 ≦ Vouт<1.7		0.14	0.32	
\/	Dranaut Valtaga	J 200 A	1.7 ≤ Vouт<1.8		0.13	0.28	V
V DIF	V _{DIF} Dropout Voltage	Іоит=300mA	1.8 ≦ Vouт<2.0		0.12	0.24]
			2.0 ≦ Vouт<2.5		0.10	0.21]
			$2.5 \le V_{\text{OUT}} \le 5.0$		0.09	0.18	
Iss	Supply Current	VIN-VOUT =1.0V, VCE=0V			130	320	μА
İstandby	Standby Current	VIN-VOUT=1.0V, VIN=VCE			0.1	2.0	μΑ
ΔVουτ/ΔVιν	Line Regulation	Іоит=200mA		,	. 4 - 4 6 - 11 - 1 - 1 - 1 - 1 - 1 - 1		
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp-p		Refer	r to the following table		able
Vin	Input Voltage			2.1		6.0	V
ΔVουτ/ ΔTopt	Output Voltage Temperature Coefficient	Iо∪т=10mA -40°C ≦ Topt ≦ 85°C			±100		ppm/°C
Інм	Output Current	VIN-VOUT=1	.0V	1.5			Α
Isc	Short Current Limit	Vout=0V			200		mA
Rpu	Pull-up resistance for \overline{CE} pin			2.5	5.0	10.0	ΜΩ
Vсен	CE Input Voltage "H"			1.2		VIN	V
Vcel	CE Input Voltage "L"			0		0.25	V
T _{TSD}	Thermal Shutdown Detector Threshold Temperature	Junction Temperature			150		°C
T _{TSR}	Thermal Shutdown Released Temperature	Junction Te	emperature		120		°C

• Line Regulation by Output Voltage

Topt=25°C

Output Voltage	Line Regulation ΔVουτ/ΔVιν (%/V)			
Vout (V) Condition		Тур.	Max.	
1.5 ≦ Vouт < 1.6	I_{OUT} =200mA, 2.1V $\leq V_{IN} \leq 6.0V$	0.05	0.30	
1.6 ≤ Vout ≤ 5.0	$I_{OUT}=200mA$, $V_{OUT}+0.5V \le V_{IN} \le 6.0V$	0.05	0.30	

• Ripple Rejection by Output Voltage

Output Voltage	Ripple Rejection RR (dB)		
V оит (V)	Vout (V) Condition		
1.5 ≦ Vouт < 4.7	f=1kHz, Ripple 0.5Vp-p, V _{IN} -V _{OUT} =1.0V	50	
4.7 ≤ Vouт ≤ 5.0	f=1kHz, Ripple 0.5Vp-p, V _{IN} =5.75V	50	

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• R1171Sxx1B Topt=25°C

Symbol	Item	Coi	nditions	Min.	Тур.	Max.	Unit
Vоит	Reference Voltage for Adjustable Voltage Regulator	VIN-VOUT=1. IOUT=200mA		×0.98		×1.02	V
ΔV ουτ/ ΔΙ ουτ	Load Regulation	$V_{IN}-V_{OUT}=1.$ $1mA \le I_{OUT}$			10	60	mV
			1.5 ≦ Vouт<1.6		0.16	0.35	
			1.6 ≦ Vouт<1.7		0.14	0.32	
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	V _{DIF} Dropout Voltage	J 200 A	$1.7 \le V_{\text{OUT}} < 1.8$		0.13	0.28	V
V DIF		Іоит=300mA	1.8 ≦ Vouт<2.0		0.12	0.24	\ \
			2.0 ≦ Vouт<2.5		0.10	0.21	
		-	$2.5 \le V_{\text{OUT}} \le 5.0$		0.09	0.18	
Iss	Supply Current	VIN-VOUT=1.0V, VCE=VIN			130	320	μА
İstandby	Standby Current	VIN-VOUT=1.0V, VCE=0V			0.1	2.0	μА
ΔVουτ/ΔVιν	Line Regulation	Іоит= 200mA		Defe	r to the following total-		tabla
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp-p		Refe	er to the following table		
Vin	Input Voltage			2.1		6.0	V
ΔV _{OUT} / ΔTopt	Output Voltage Temperature Coefficient	Iou τ =10mA -40°C ≤ Topt ≤ 85°C			±100		ppm/°C
Інм	Output Current	VIN-VOUT=1.	0V	1.5			Α
Isc	Short Current Limit	Vouт=0V			200		mA
R _{PD}	Pull-down resistance for CE pin				5.0	10.0	MΩ
VCEH	CE Input Voltage "H"			1.2		VIN	V
VCEL	CE Input Voltage "L"			0		0.25	V
Trsp	Thermal Shutdown Detector Threshold Temperature	Junction Temperature			150		°C
Trsr	Thermal Shutdown Released Temperature	Junction Te	mperature		120		°C

• Line Regulation by Output Voltage

Topt=25°C

Output Voltage	Line Regulation ΔV		
V оит (V)	Condition	Тур.	Max.
1.5 ≦ Vouт < 1.6	$I_{OUT}=200mA$, $2.1V \le V_{IN} \le 6.0V$	0.05	0.20
1.6 ≦ V _{OUT} ≦ 5.0	$I_{OUT}=200$ mA, $V_{OUT}+0.5$ V $\leq V_{IN} \leq 6.0$ V	0.05	0.30

• Ripple Rejection by Output Voltage

Output Voltage	Ripple Rejection RR (dB)		
V оит (V)	Vour (V) Condition		
$1.5 \leq V_{\text{OUT}} < 4.7$	f=1kHz, Ripple 0.5Vp-p, V _{IN} -V _{OUT} =1.0V	50	
$4.7 \le V_{\text{OUT}} \le 5.0$	f=1kHz, Ripple 0.5Vp-p, V _{IN} =5.75V	50	

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• R1171Jxx1C

Topt=25°C

Symbol	Item	Coi	nditions	Min.	Тур.	Max.	Unit
Vоит	Output Voltage	VIN-VOUT=1.0 IOUT=200mA)V	×0.98		×1.02	V
ΔV ουτ/ ΔΙ ουτ	Load Regulation	V _{IN} -V _{OUT} =1.0 1mA ≦ I _{OUT} ≦			10	60	mV
			1.8 ≦ Vouт<2.0		0.12	0.24	
VDIF	Dropout Voltage	Iout=300mA	2.0 ≦ Vou т< 2.5		0.10	0.21	V
			$2.5 \le V_{\text{OUT}} \le 5.0$		0.09	0.18	
Iss	Supply Current	VIN-VOUT=1.0	OV, VCE=OV		130	320	μА
Istandby	Standby Current	VIN-VOUT =1.	OV, VIN=VCE		0.1	2.0	μА
ΔV out/ ΔV in	Line Regulation	I_{OUT} =200mA V_{OUT} +0.5V $\leq V_{IN} \leq 6V$			0.05	0.30	%/V
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp-p		Refe	Refer to the following table		table
Vin	Input Voltage			2.1		6.0	V
ΔVουτ/ ΔTopt	Output Voltage Temperature Coefficient	$I_{OUT}=10mA$ $-40^{\circ}C \le T_{opt} \le 85^{\circ}C$			±100		ppm/°C
Інм	Output Current	V _{IN} -V _{OUT} =1.0V		2.0			Α
Isc	Short Current Limit	Vout=0V			200		mA
Rpu	Pull-up resistance for $\overline{\sf CE}$ pin			2.5	5.0	10.0	MΩ
VCEH	CE Input Voltage "H"			1.2		VIN	V
VCEL	CE Input Voltage "L"			0		0.25	V
Ттѕр	Thermal Shutdown Detector Threshold Temperature	Junction Temperature			150		°C
Ttsr	Thermal Shutdown Released Temperature	Junction Ter	mperature		120		°C

• Ripple Rejection by Output Voltage

Output Voltage	Ripple Rejection RR (dB)		
V оит (V)	Condition	Тур.	
$1.8 \le V_{OUT} < 4.7$	f=1kHz, Ripple 0.5Vp-p, V _{IN} -V _{OUT} =1.0V	- 50	
$4.7 \le V_{\text{OUT}} \le 5.0$	f=1kHz, Ripple 0.5Vp-p, V _{IN} =5.75V		

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• R1171Jxx1D

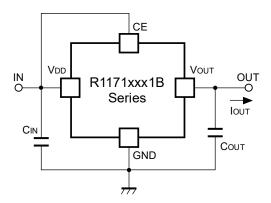
Topt=25°C

Symbol	Item	Conditions		Min.	Тур.	Max.	Unit
Vоит	Reference Voltage for Adjustable Voltage Regulator	V _{IN} -V _{OUT} =1.0V I _{OUT} =200mA		×0.98		×1.02	V
ΔV ουτ/ ΔΙ ουτ	Load Regulation	V_{IN} - V_{OUT} =1.0 V 1 $mA \le I_{OUT} \le 300mA$			10	60	mV
Voif	Dropout Voltage	Іоит=300mА	1.8 ≤ V _{OUT} <2.0		0.12	0.24	V
			$2.0 \le V_{\text{OUT}} < 2.5$		0.10	0.21	
			$2.5 \le V_{\text{OUT}} \le 5.0$		0.09	0.18	
Iss	Supply Current	VIN-VOUT=1.0V, VCE=VIN			130	320	μА
İstandby	Standby Current	VIN-VOUT=1.0V, VCE=0V			0.1	2.0	μА
ΔVout/ ΔVin	Line Regulation	$I_{OUT}=200mA$ $V_{OUT}+0.5V \le V_{IN} \le 6V$			0.05	0.30	%/V
RR	Ripple Rejection	f=1kHz, Ripple 0.5Vp-p		Refe	efer to the following table		
VIN	Input Voltage			2.1		6.0	V
ΔVουτ/ ΔTopt	Output Voltage Temperature Coefficient	Iout=10mA -40°C ≤ Topt ≤ 85°C			±100		ppm/°C
ILIM	Output Current	Vin-Vout=1.0V		2.0			Α
Isc	Short Current Limit	Vout=0V			200		mA
RPD	Pull-down resistance for CE pin			2.5	5.0	10.0	MΩ
Vceh	CE Input Voltage "H"			1.2		VIN	V
VCEL	CE Input Voltage "L"			0		0.25	V
T _{TSD}	Thermal Shutdown Detector Threshold Temperature	Junction Temperature			150		°C
T _{TSR}	Thermal Shutdown Released Temperature	Junction Temperature			120		°C

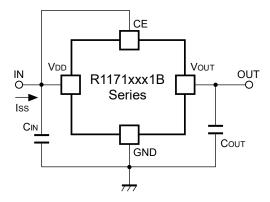
• Ripple Rejection by Output Voltage

Output Voltage	Ripple Rejection RR (dB)		
V out (V)	Condition	Тур.	
1.8 ≤ V _{OUT} < 4.7	f=1kHz, Ripple 0.5Vp-p, V _{IN} -V _{OUT} =1.0V	50	
$4.7 \le V_{\text{OUT}} \le 5.0$	f=1kHz, Ripple 0.5Vp-p, V _{IN} =5.75V	50	

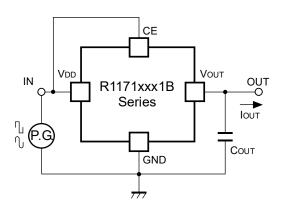
TEST CIRCUITS



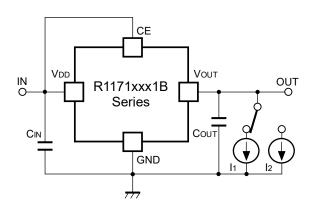
Standard Test Circuit



Supply Current Test Circuit



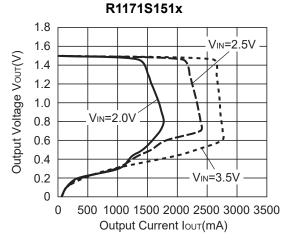
Test Circuit for Ripple Rejection, Input Transient Response

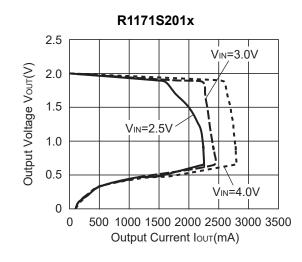


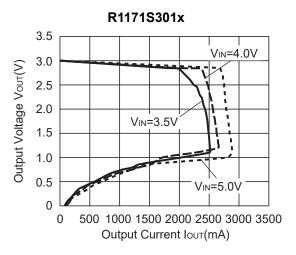
Test Circuit for Load Transient Response

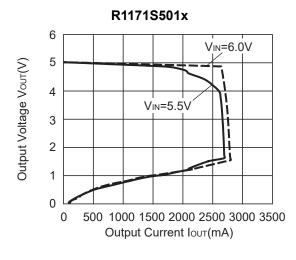
TYPICAL CHARACTERISTICS

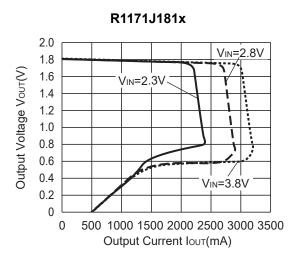
1) Output Voltage vs. Output Current (Topt=25°C)

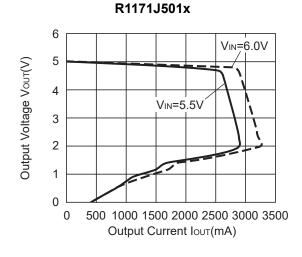




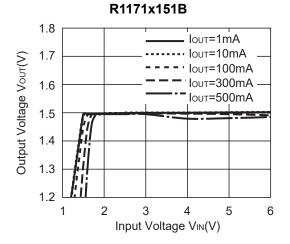


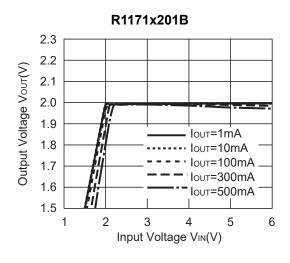


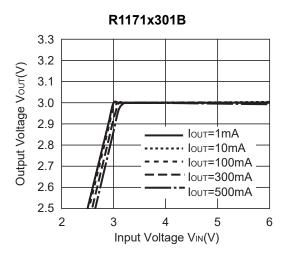


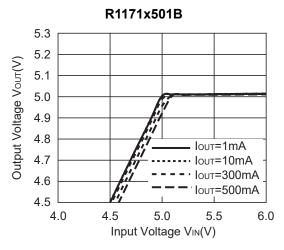


2) Output Voltage vs. Input Voltage (Topt=25°C)

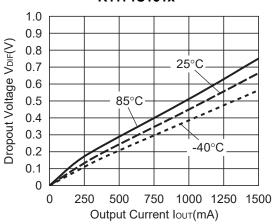


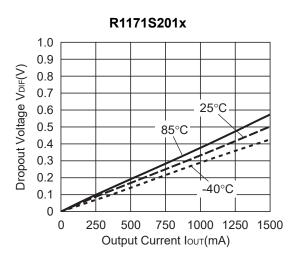




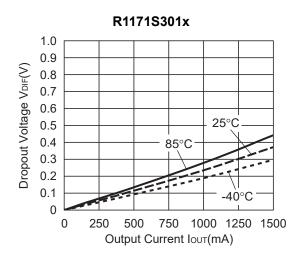


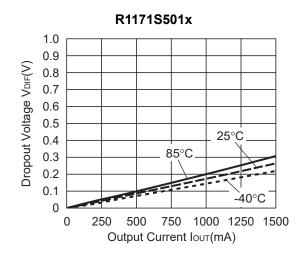
3) Dropout Voltage vs. Output Current R1171S151x

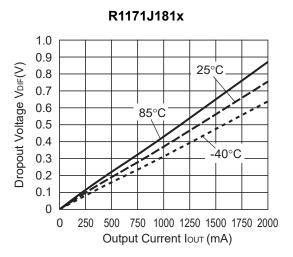


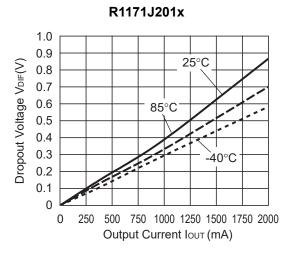


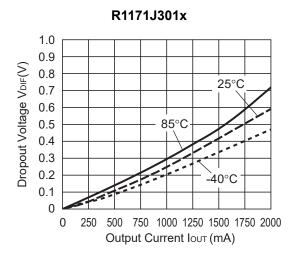
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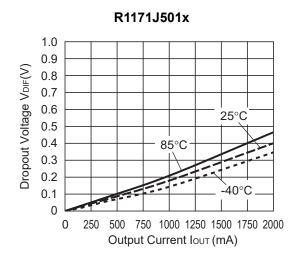






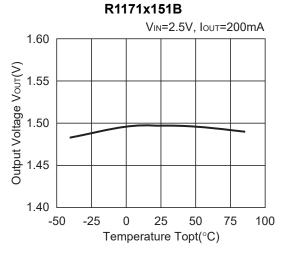


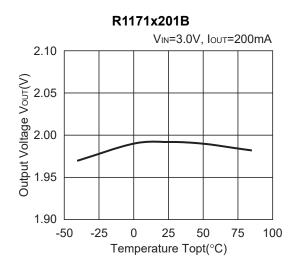




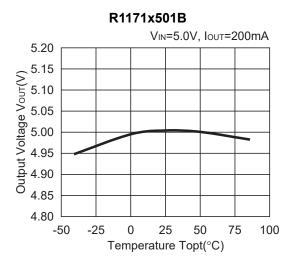
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4) Output Voltage vs. Temperature

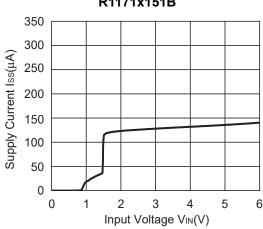


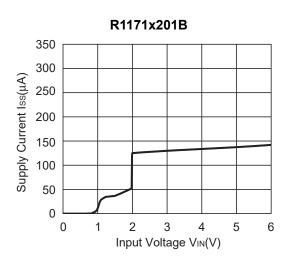


R1171x301B VIN=4.0V, IOUT=200mA 3.20 3.15 Output Voltage Vour(V) 3.10 3.05 3.00 2.95 2.90 2.85 2.80 -25 0 25 50 75 100 -50 Temperature Topt(°C)

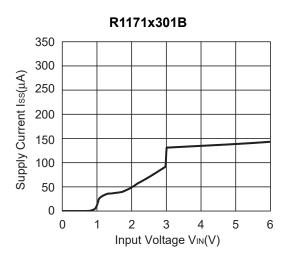


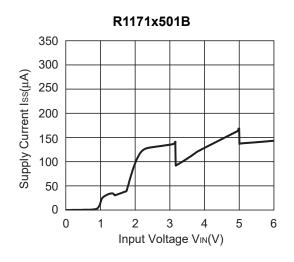
5) Supply Current vs. Input Voltage (Topt=25°C) R1171x151B



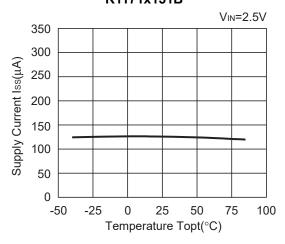


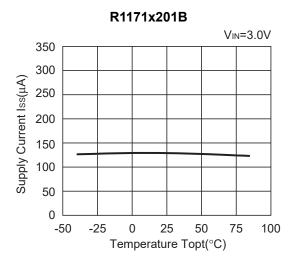
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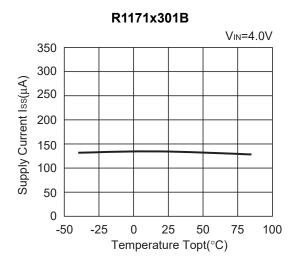


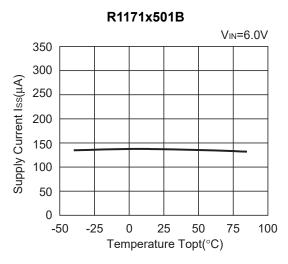


6) Supply Current vs. Temperature R1171x151B

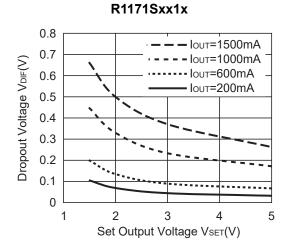


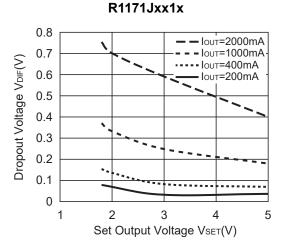




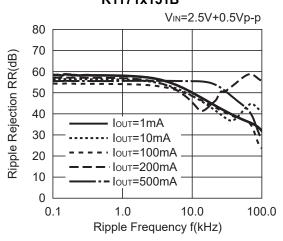


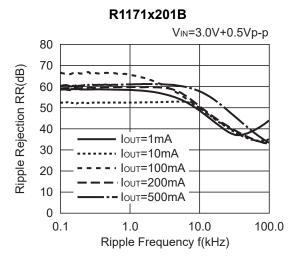
7) Dropout Voltage vs. Set Output Voltage (Topt=25°C)

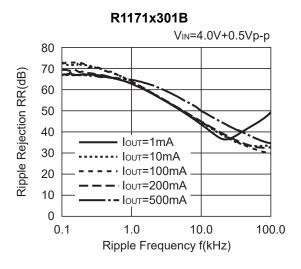


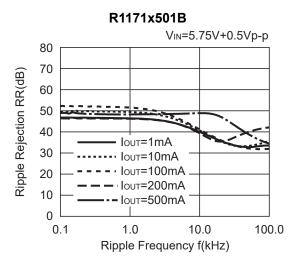


8) Ripple Rejection vs. Frequency R1171x151B





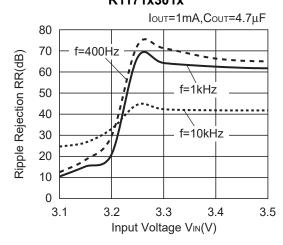


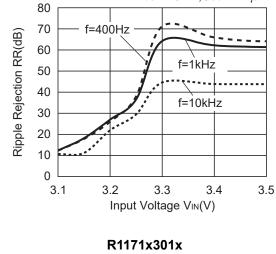


NO. EA-125-220908

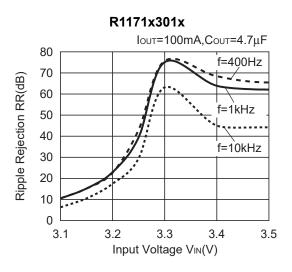
IOUT=10mA,COUT= $4.7\mu F$

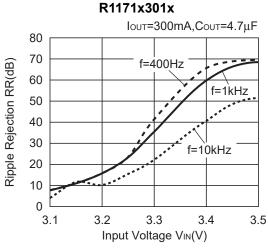
9) Ripple Rejection vs. Input Voltage R1171x301x

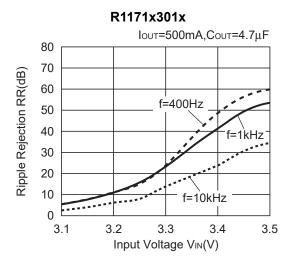




R1171x301x



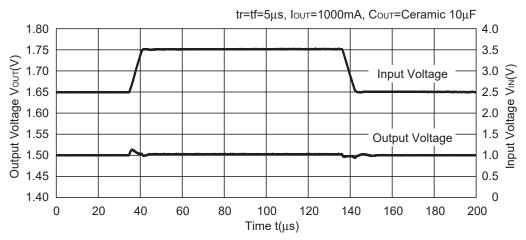




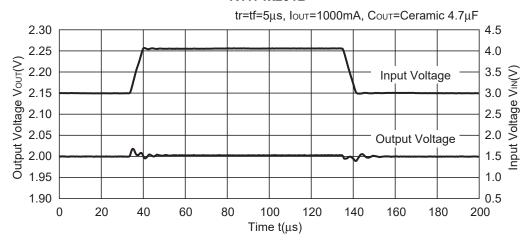
NO. EA-125-220908

10) Input Transient Response (Topt=25°C)

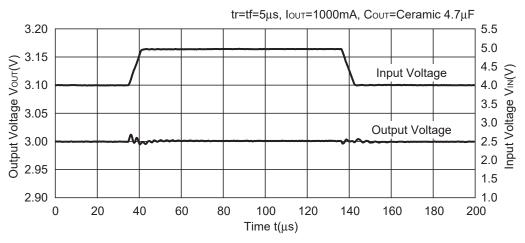
R1171x151B



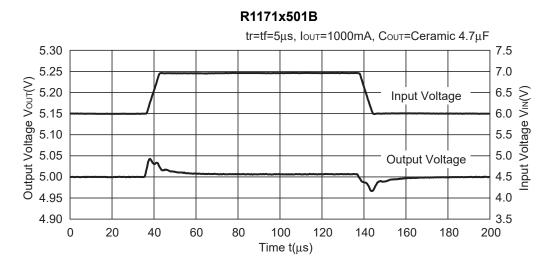
R1171x201B



R1171x301B

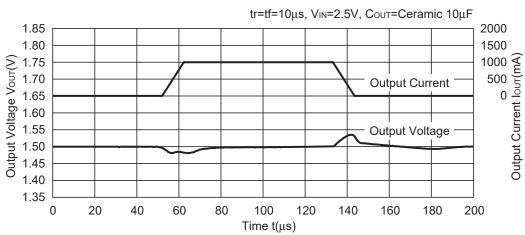


NO. EA-125-220908

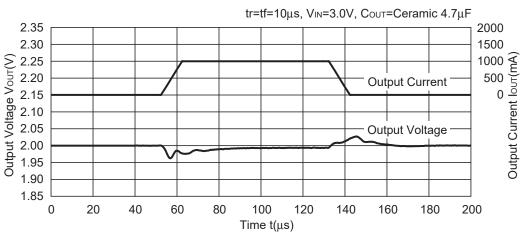


11) Load Transient Response (Topt=25°C)

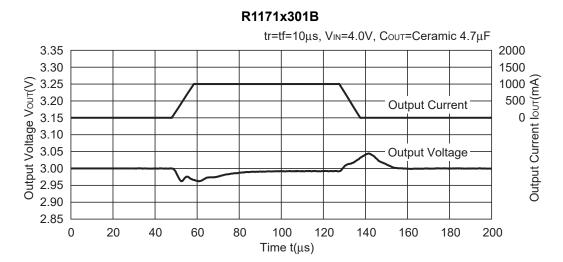
R1171x151B



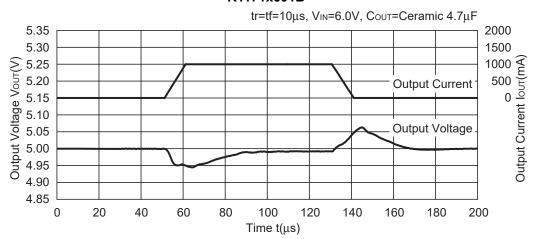
R1171x201B



NO. EA-125-220908



R1171x501B



NO. EA-125-220908

Technical Notes on External Components and Typical Application

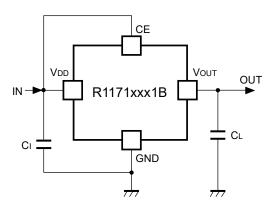
1. Phase Compensation

In these ICs, phase compensation is made with the output capacitor for securing stable operation even if the load current is varied. For this purpose, use a capacitor with the capacitance range from $4.7\mu F$ to $10.0\mu F$, as C_L . In case that using a tantalum capacitor and the ESR of the tantalum capacitor is too large, unstable operation may result. Fully evaluation is necessary for the whole circuit with considering the frequency characteristic.

2. Mounting on PCB

Make V_{DD} and GND lines sufficient. If their impedance is high, large current may flow and the pick-up noise or unstable operation may result. Therefore use a capacitor with a capacitance range from $4.7\mu F$ to $10.0\mu F$ between V_{DD} pin and GND pin as close as possible.

Further, set an output capacitor between VouT pin and GND pin for phase compensation as close as possible. (Refer to the example of typical application)



R1171Sxx1B Typical Application

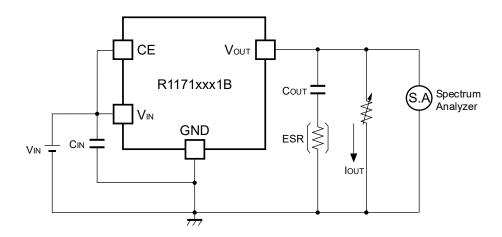
 $1.5V \le V_{\text{OUT}} < 1.8V$: $C = 10 \mu F$ (Ceramic), $C_L = 10 \mu F$ (Ceramic) $1.8V \le V_{\text{OUT}} \le 5.0V$: $C = 4.7 \mu F$ (Ceramic), $C_L = 4.7 \mu F$ (Ceramic)

3. Output Short Protection Function

In the R1171x Series, the output short protection function is built in, further, if the output is short to the GND or other voltage line, the chip inside is heating, as a result, in case that the junction temperature becomes equal or more than 150°C (Typ.), the built-in thermal shutdown circuit works. If the junction temperature becomes equal or more than 150°C (Typ.), the IC is protected by the output short protection circuit and the thermal shutdown circuit.

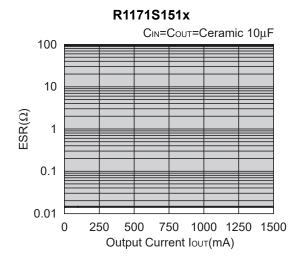
NO. EA-125-220908

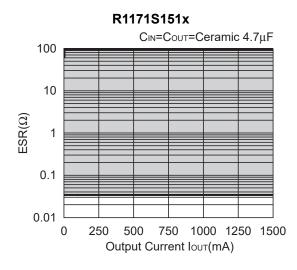
ESR vs. Output Current (Topt=25°C, VIN=Set Output Voltage+1V, CIN=Ceramic 10μF)



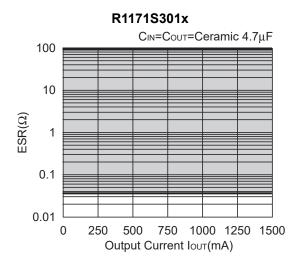
As an output capacitor for this IC, Ceramic capacitor is recommendable. However, other low ESR type capacitor can be used with this IC.

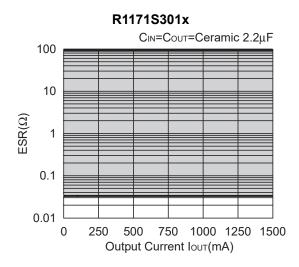
For your reference, noise level is tested with the circuit as shown above, and if the noise level is $40\mu V$ or less than $40\mu V$, the ESR values are plotted as stable area. Upper limit is described in the next four graphs, or ESR vs. Output Current. (Hatched area is the stable area.)

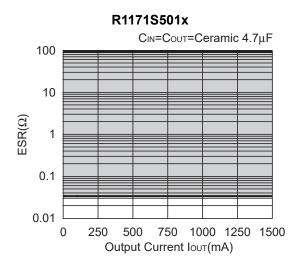


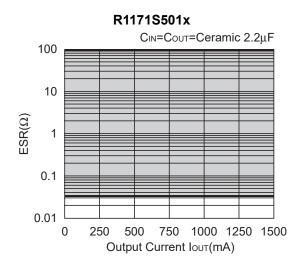


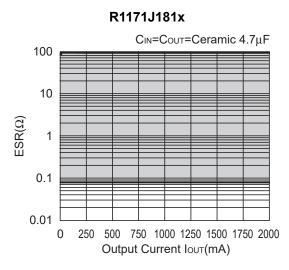
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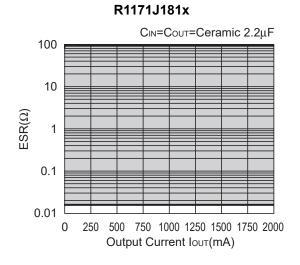






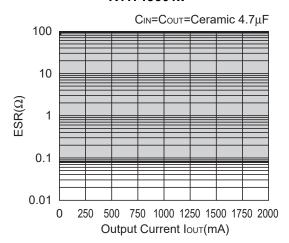




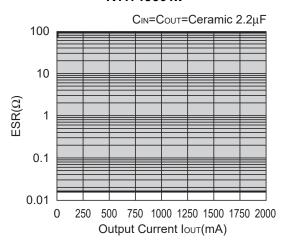


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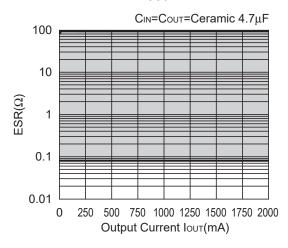
R1171J301x



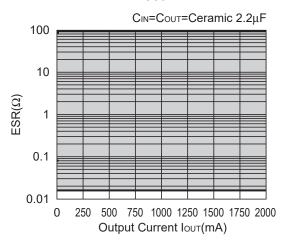
R1171J301x



R1171J501x



R1171J501x



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 - When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.
 - Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
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