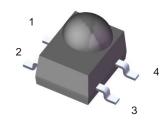


DATASHEET

Infrared Receiver Control Receiver Module IRM-H600JW/TR2

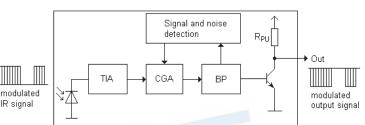


Pin Configuration

IR signal

- 1. GND
- 2. GND
- 3. Out
- 4. Vcc

Block Diagram



Features

- · High protection ability against EMI
- · Circular lens for improved reception characteristics
- Available for Carrier Frequencies between 20KHz to 60KHz
- TTL and CMOS Compatible
- Low operating voltage (Vcc = 2.7V)
- · High immunity against ambient light
- Long reception range
- · High sensitivity
- · Pb free and RoHS compliant

Description

The IRM-H600JW devices are miniature type infrared receivers which have been developed and designed by using the latest IC technology.

The photo diode and preamplifier are assembled onto a lead frame and molded into an epoxy package which operates as an IR filter.

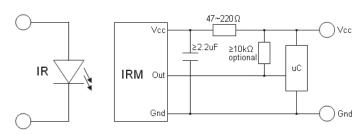
The receiver provides a modulated output signal which can be used for IR code learning and IR repeater.

Applications

- · IR code learning
- IR repeater
- Remote control



Application Circuit



Parts Table

Model No.	Carrier Frequency		
IRM-H600T/TR2	20∼60 kHz		

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	Vcc	6	V
Operating Temperature	Topr	-20 ~ +85	$^{\circ}$ C
Storage Temperature	Tstg	-40 ~ +85	$^{\circ}\! \mathbb{C}$
Soldering Temperature *1	Tsol	260	$^{\circ}\! \mathbb{C}$

^{*1 4}mm from mold body for less than 10 seconds

Electro-Optical Characteristics (Ta=25°℃)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Condition
Current consumption	Icc		0.4	0.5	mA	No input signal
Supply voltage	V _{CC}	2.7	-	5.5	V	
Reception range	L ₀	8			m	See chapter
	L ₄₅	5			m	,Test method'
Half angle(horizontal)	φ _h		±45		deg	Test signal according to figure 1
Half angle(vertical)	ϕ_{v}		±45		deg	
High level output voltage	V_{OH}	Vcc-0.4			V	
Low level output voltage	V_{OL}		0.2	0.5	V	$I_{SINK} \leq 2mA$
Internal pull up resistor	R_{PU}	46	52	58	kΩ	



D.U.T output Pulse

Test method

The specified electro-optical characteristics are valid under the following conditions.

- 1. Measurement environment
 - A place without extreme light reflections.
- 2. External light

The environment contains an ordinary, white fluorescent lamp without high frequency modulation. The color temperature is 2856K and the illumination at the IR receiver is less than 10 Lux ($Ev \le 10Lux$).

- 3. Standard transmitter
 - The test transmitter is calibrated by using the circuit shown in figure 2. The radiation intensity of the transmitter is adjusted until **Vo=400mVp-p.** Both, the test transmitter and the photo diode, have a peak wavelength of 940nm. The photo diode for calibration is PD438B ($\lambda p=940$ nm, Vr=5V).
- 4. The measurement system is shown in Fig.-3

100kΩ

Fig.-1 Transmitter Wave Form

carrier frequency is adjusted to center frequency of IRM νон IR transmitter IRM output signal output signal 600us 600us Fig.-3 Measuring System 10kΩ +5.0+-0.1V L: transmission distance O Vcc 10uF standard standard transmitter Out D.U.T. transmitter

Gnd

Ψ angle of horizontal

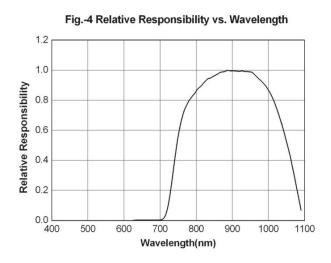
and vertical direction

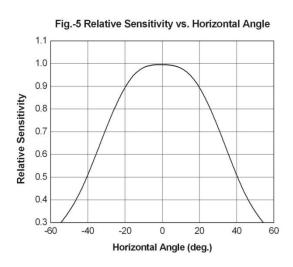
Vout

Oscilloscope



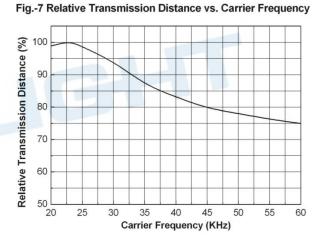
Typical Electro-Optical Characteristics Curves





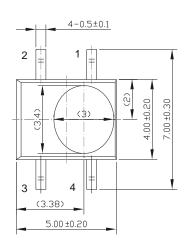
1.2
1.0
0.6
0.6
0.0
2.5
3.0
3.5
4.0
4.5
5.0
5.5
6.0

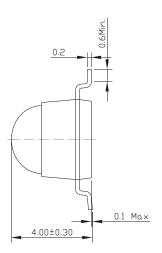
Supply Voltage Vcc (V)

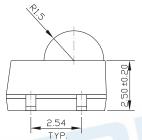




Package Dimension







Pin Configuration

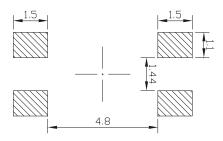
- 1. GND
- 2. GND
- 3. Out
- 4. Vcc

(Dimensions in mm)

Notes:

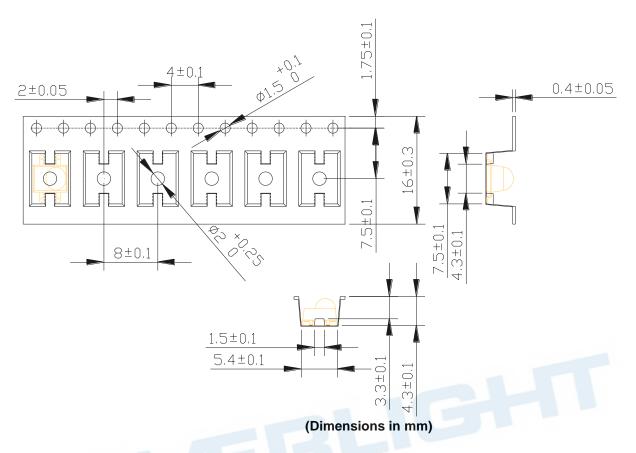
Tolerance unless otherwise mentioned ±0.3mm

Recommended pad layout for surface mount leadform





Tape & Reel Packing Specifications

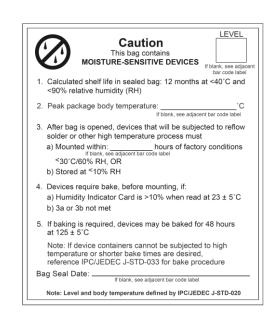


Packing Quantity

1000 pcs / Reel 5 Reel / Carton

Label format





Moisture Classification-storage and used condition label



Recommended method of storage

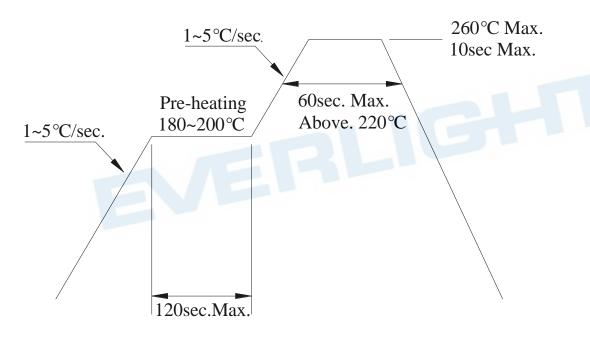
The following are general recommendations for moisture sensitive level (MSL) 4 storage and use:

- 1. Shelf life in sealed bag from the bag seal date: 12 months at < 40 °C and < 90% relative humidity (RH)
- 2. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must mounted within 72 hours of factory conditions < 30 °C/60%RH.
- 3. If the moisture absorbent material (silica gel) has faded away or the IRM has exceeded the storage time. Baking treatment is required, refer to IPC/JEDEC J-STD-033 for bake procedure or recommend the conditions: 60±5°C for 96 hours.

ESD Precaution

Proper storage and handing procedures should be followed to prevent ESD damage to the devices especially when they are removed from the Anti-static bag. Electro-Static Sensitive Devices warning labels are on the packing.

Solder Reflow Temperature Profile



Note:

- 1. Reflow soldering should not be done more than two times.
- 2. When soldering, do not put stress on the IRM device during heating.
- 3. After soldering, do not warp the circuit board.



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